

# Introduction to Computer Vision



## Lecture 8 - Deep Learning V

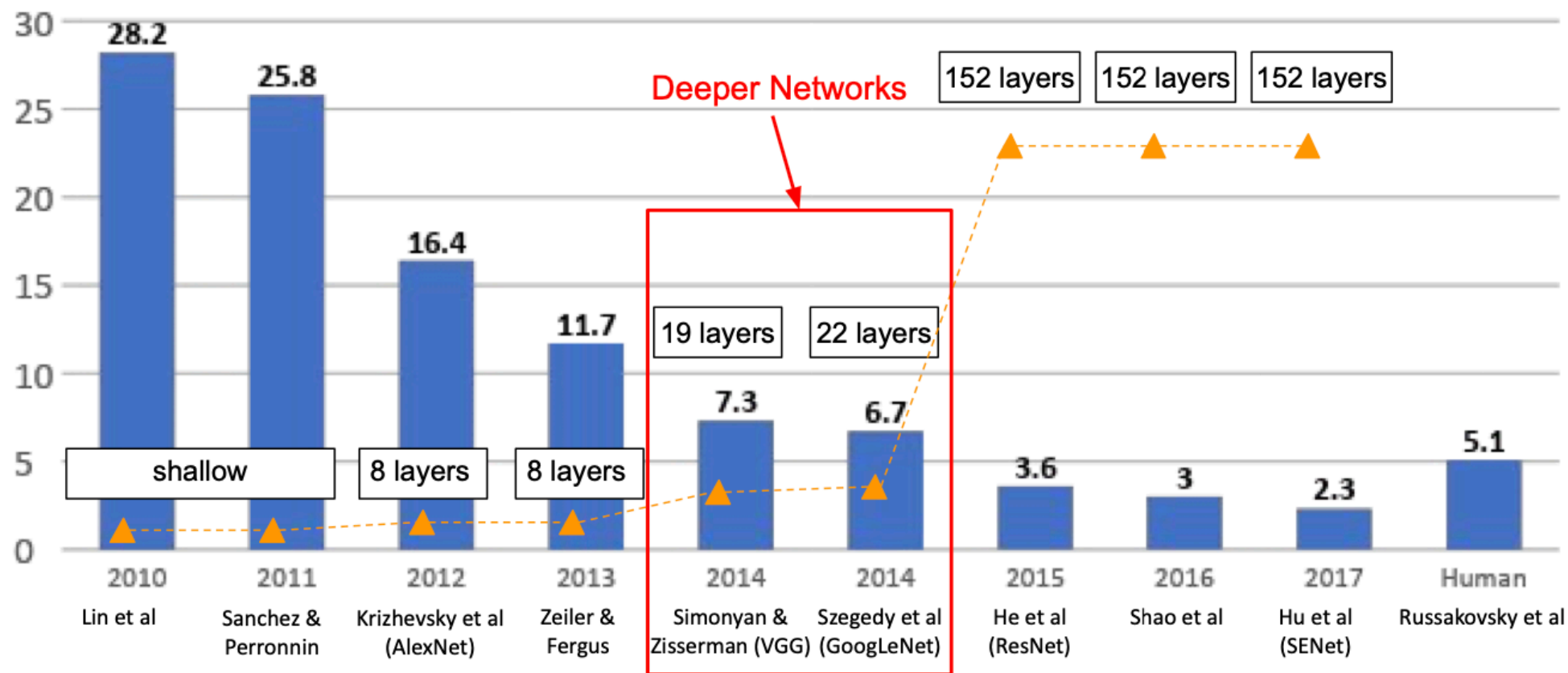
Prof. He Wang



# Logistics

- Assignment 2: to release on 4/11 (this Friday evening), due on 4/26 11:59PM (Saturday)
- Some functions are required to be implemented without for loop.
- If 1 day (0 - 24 hours) past the deadline, 15% off
- If 2 day (24 - 48 hours) past the deadline, 30% off
- Zero credit if more than 2 days.

# The History: ImageNet Challenge Winners



# VGGNet

Small filters, Deeper networks

8 layers (AlexNet)

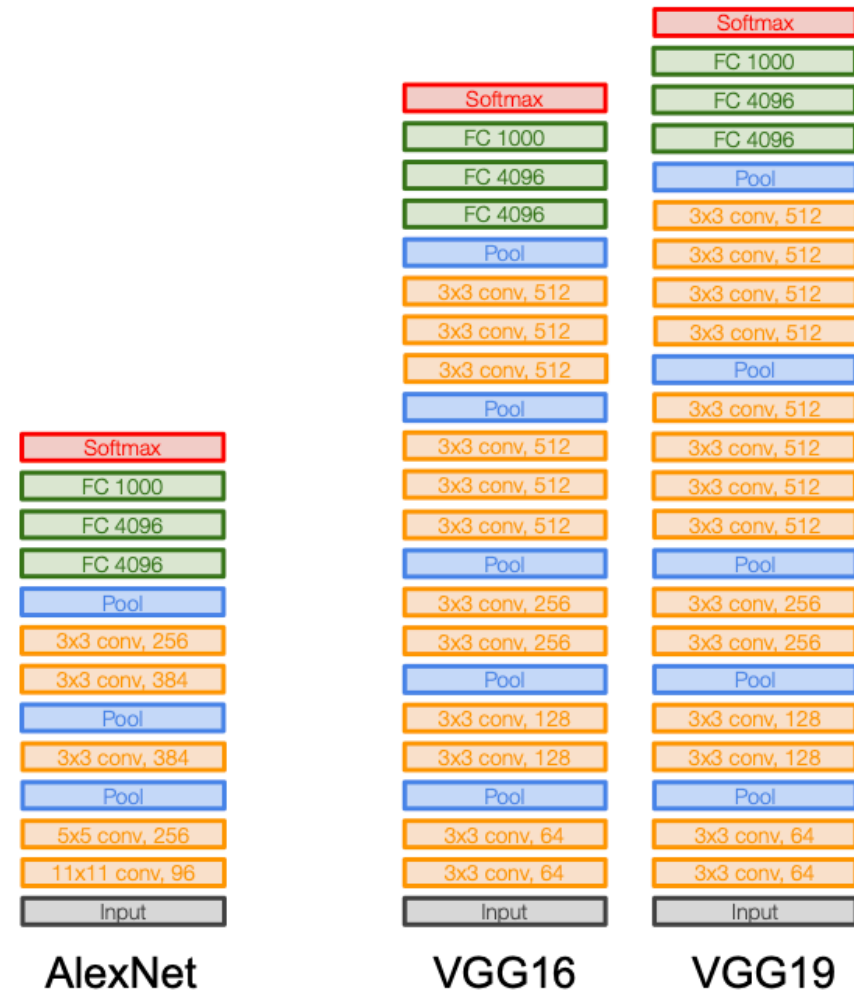
-> 16 - 19 layers (VGG16Net)

Only 3x3 CONV stride 1, pad 1  
and 2x2 MAX POOL stride 2

11.7% top 5 error in ILSVRC'13 (ZFNet)

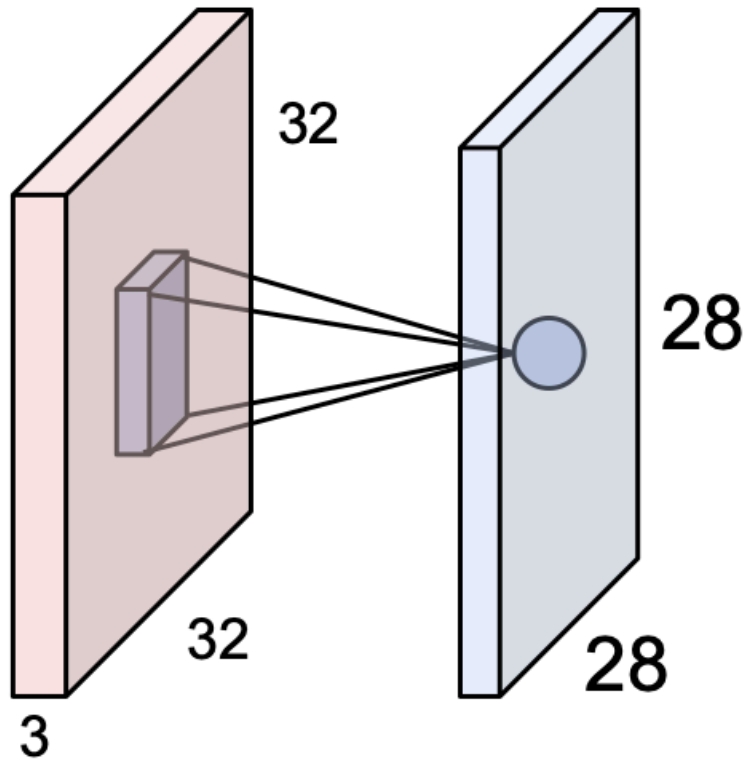
-> 7.3% top 5 error in ILSVRC'14

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# Receptive Field



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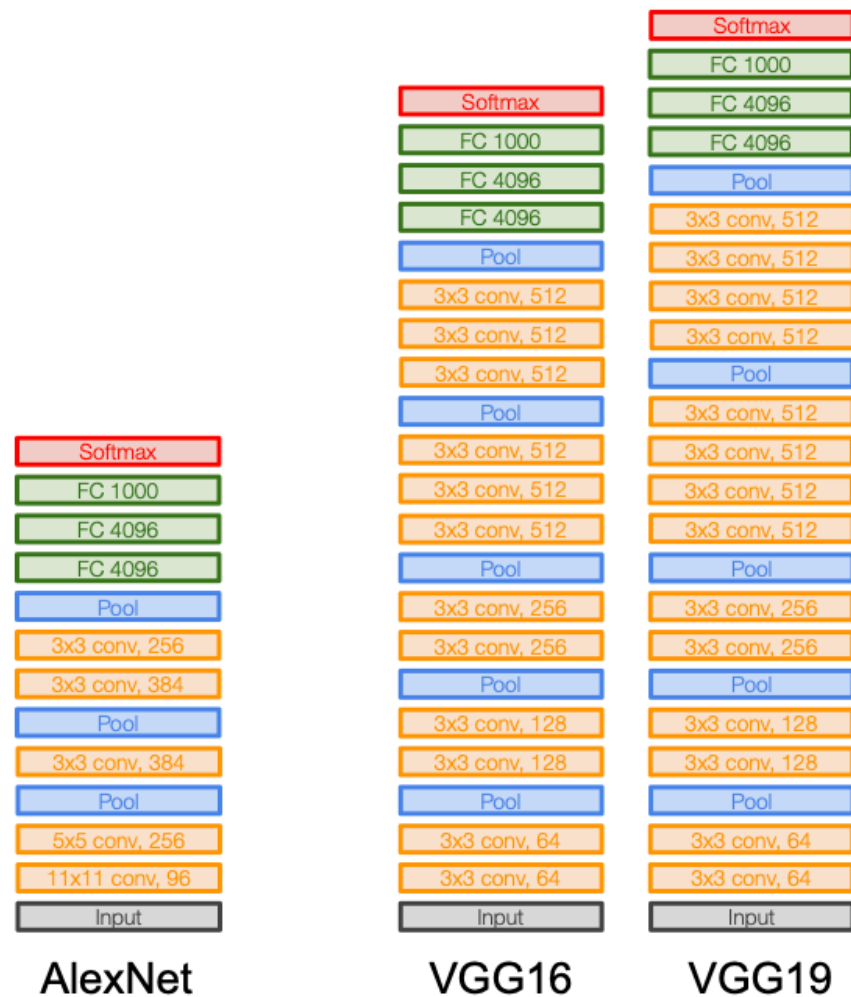
“5x5 filter” -> “5x5 receptive field for each neuron”

# VGGNet

Q: Why use smaller filters? (3x3 conv)

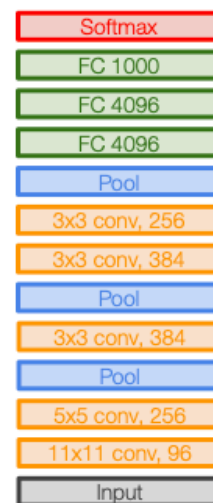
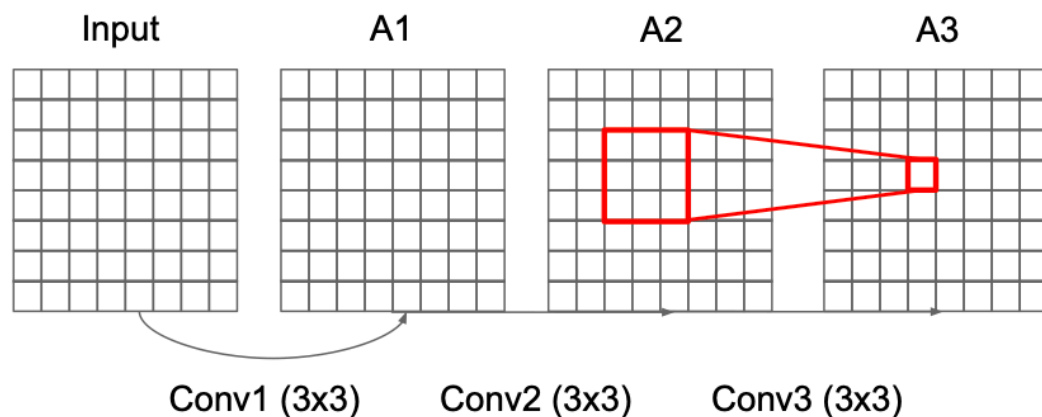
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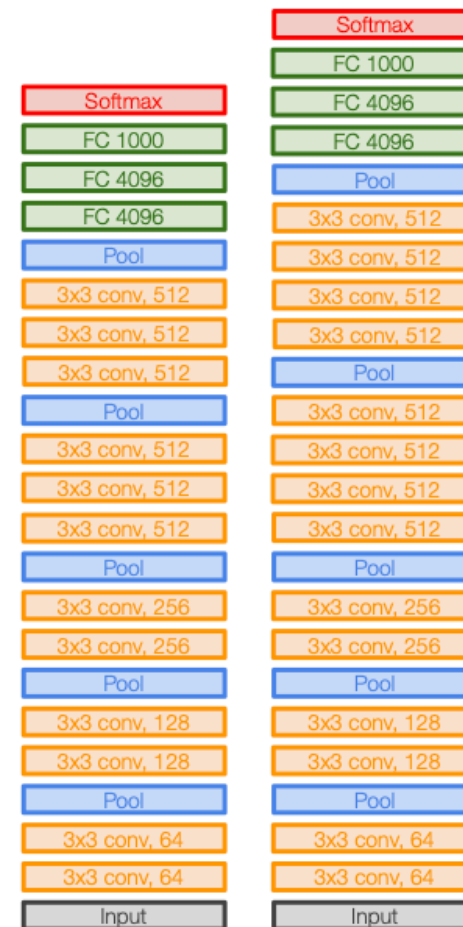


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AlexNet

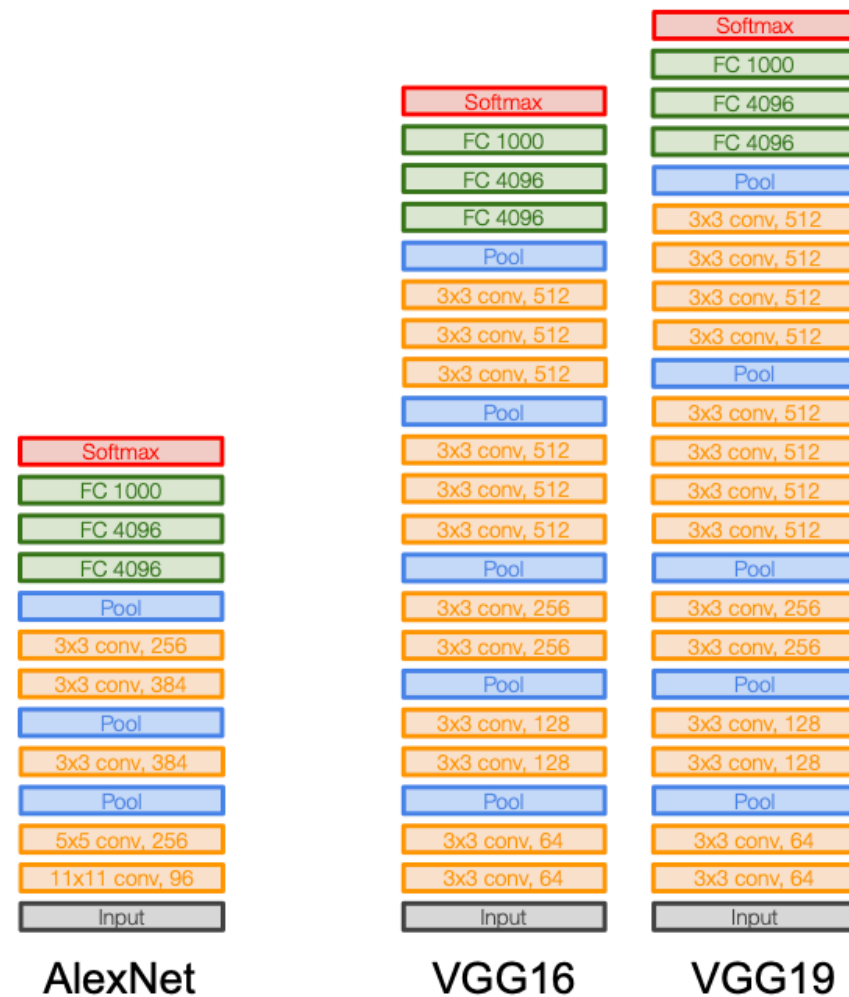
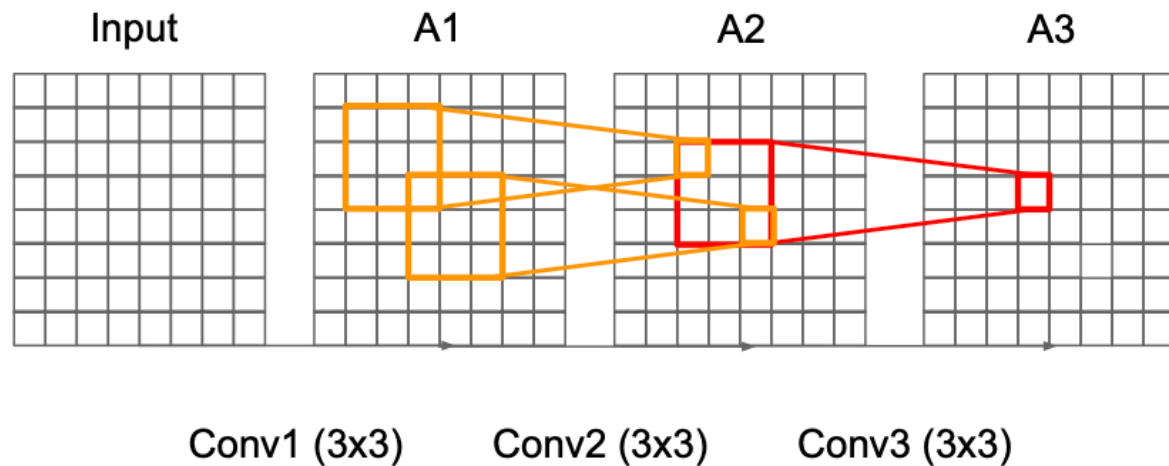


VGG16

VGG19

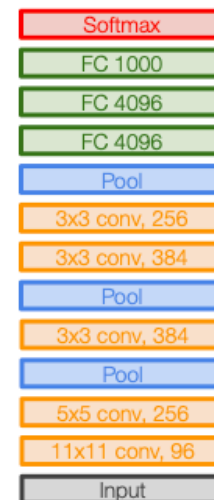
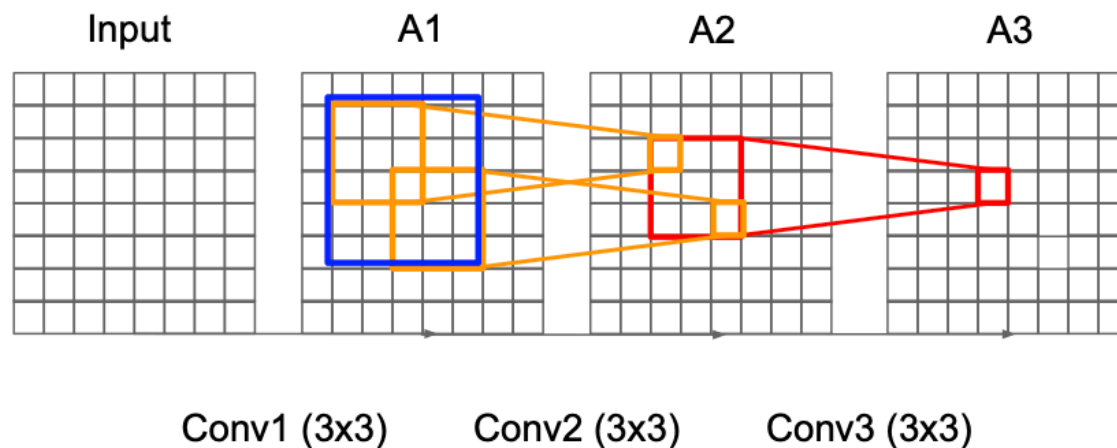
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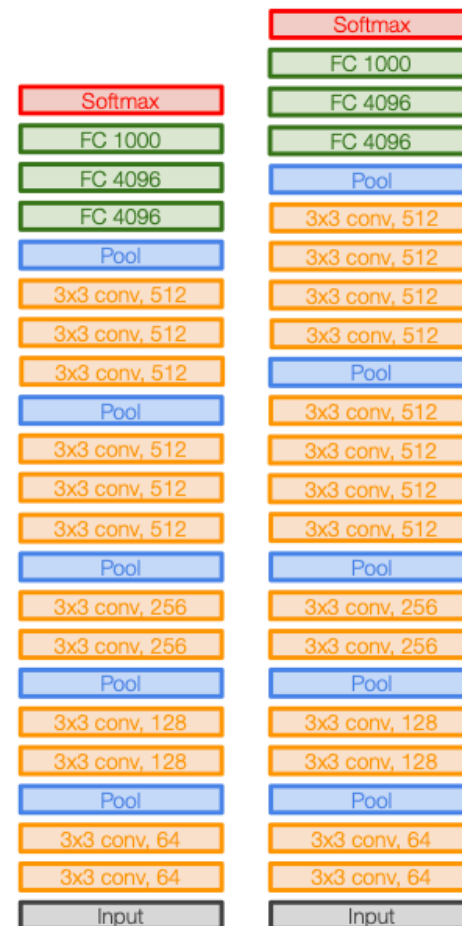


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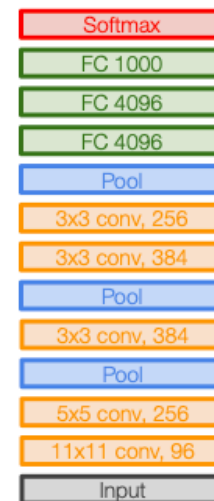
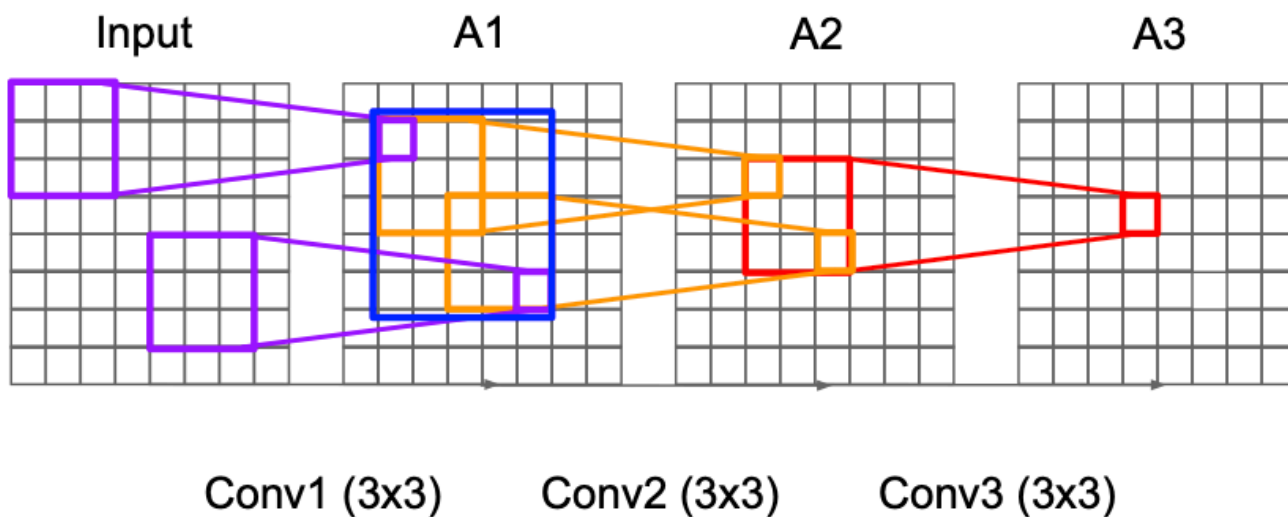


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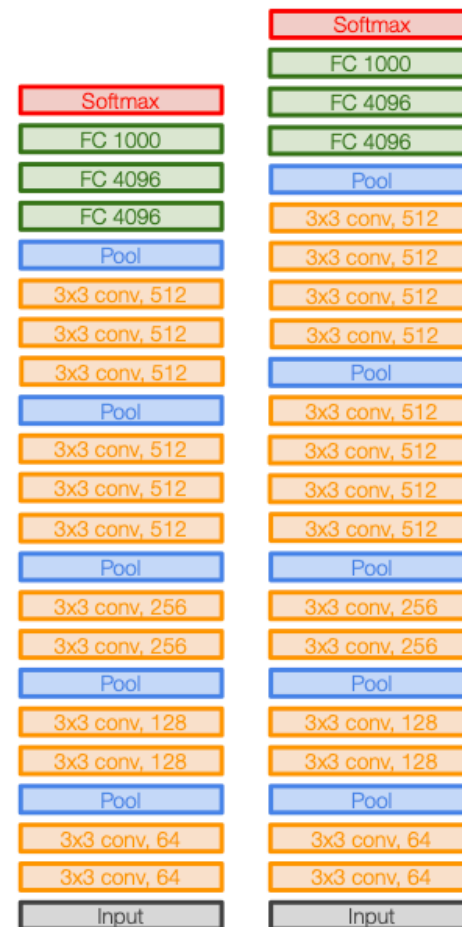
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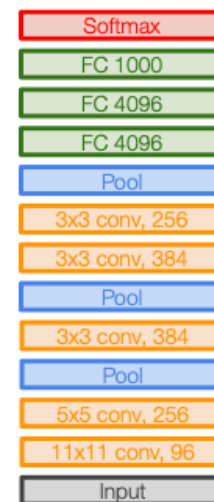
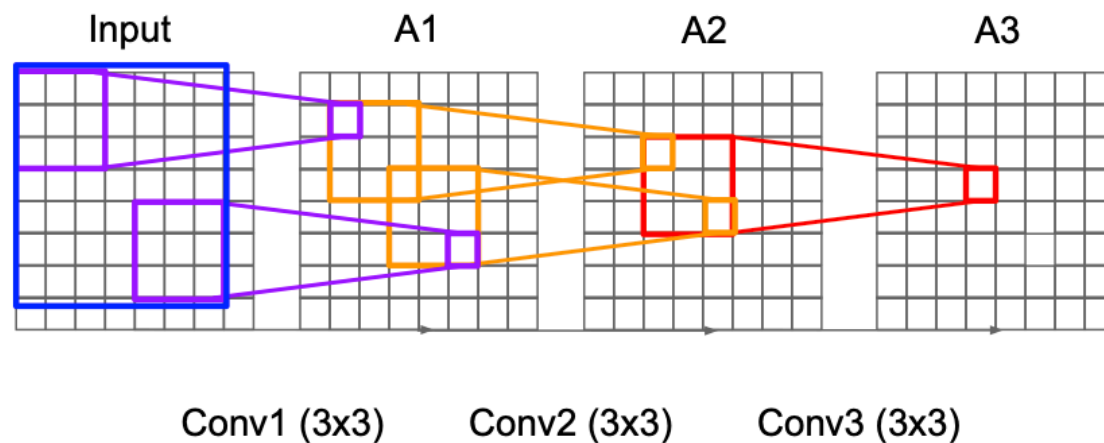


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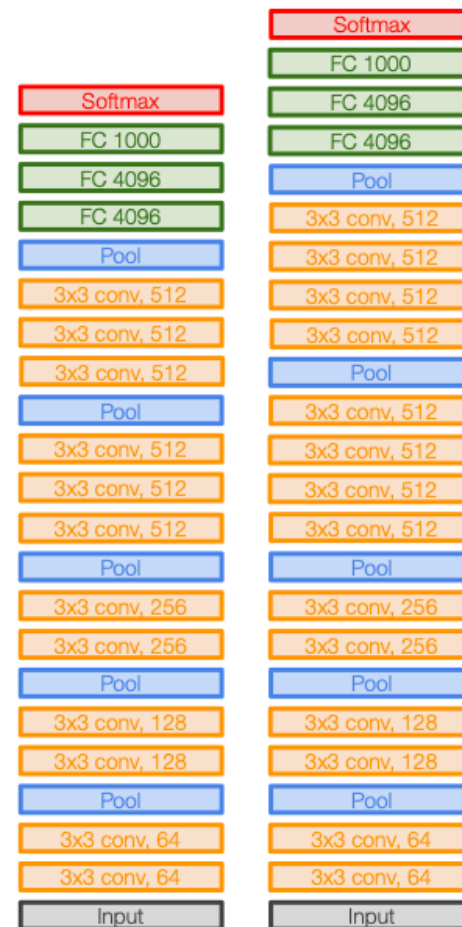
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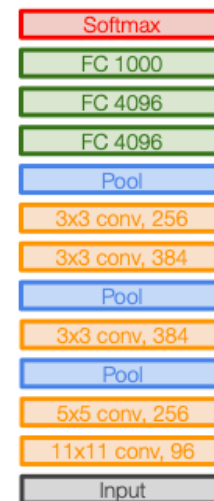
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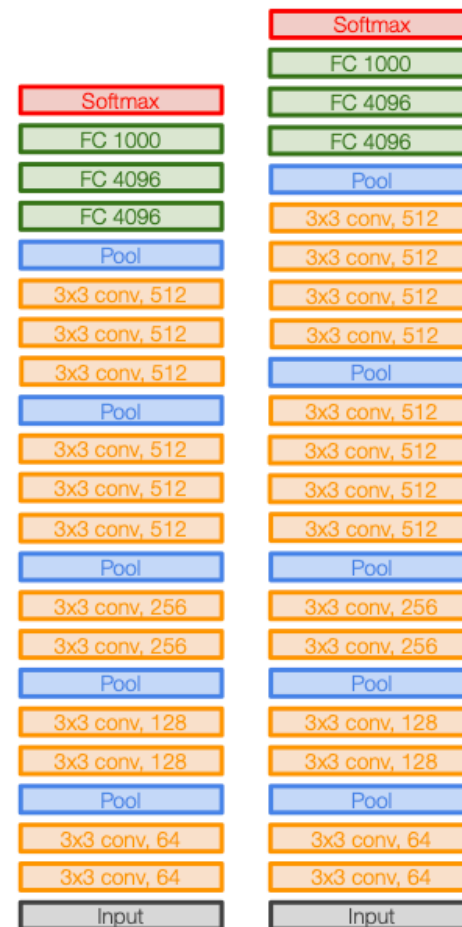
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[7x7]



AlexNet



VGG16

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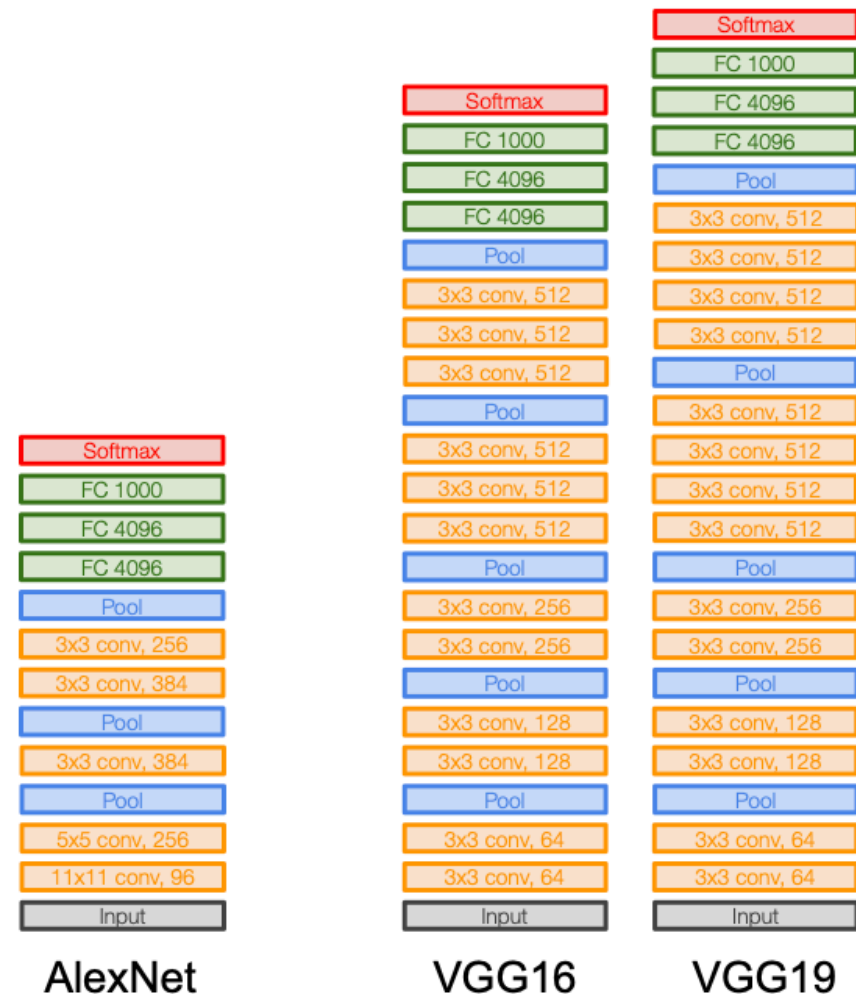
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And fewer parameters:  $3 * (3^2 C^2)$  vs.  $7^2 C^2$  for C channels per layer



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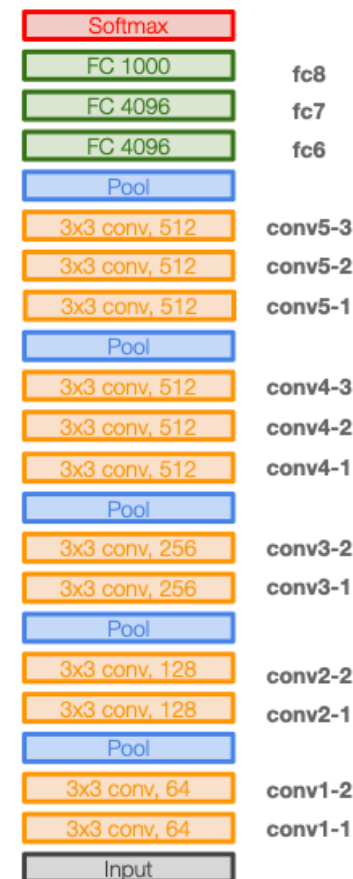
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**TOTAL memory:**  $24\text{M} * 4 \text{ bytes} \approx 96\text{MB}$  / image (only forward!  $\sim 2$  for bwd)

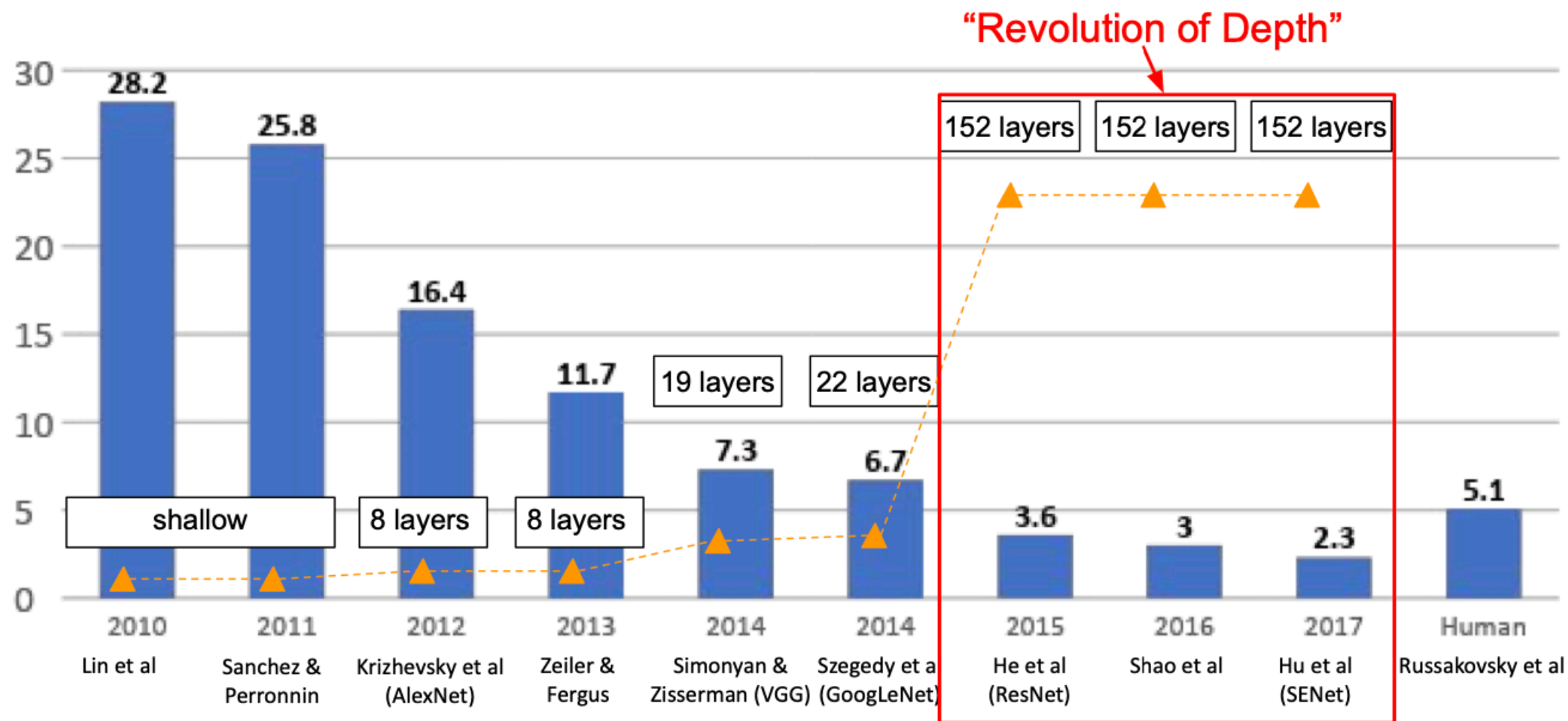
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VGG16

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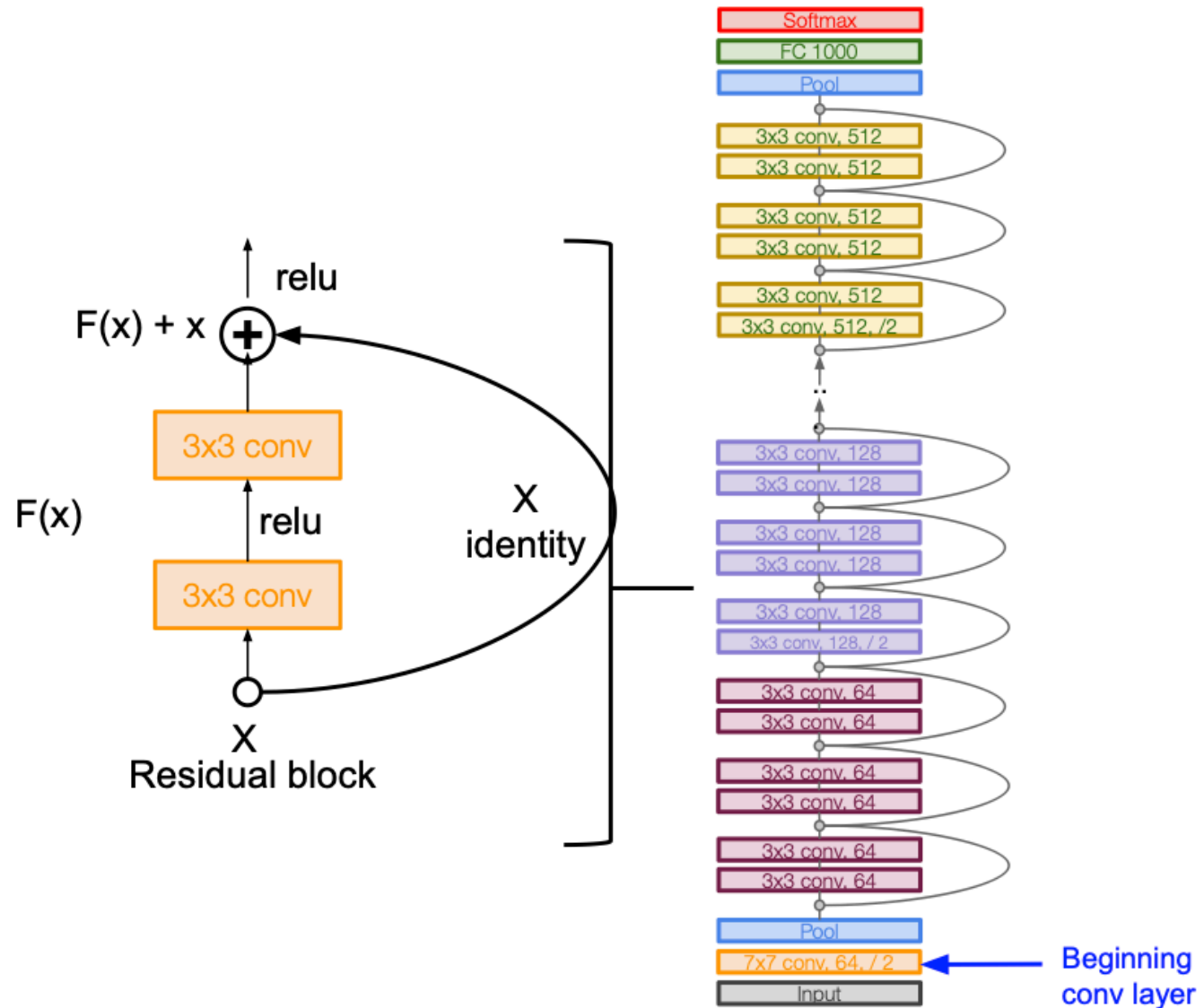
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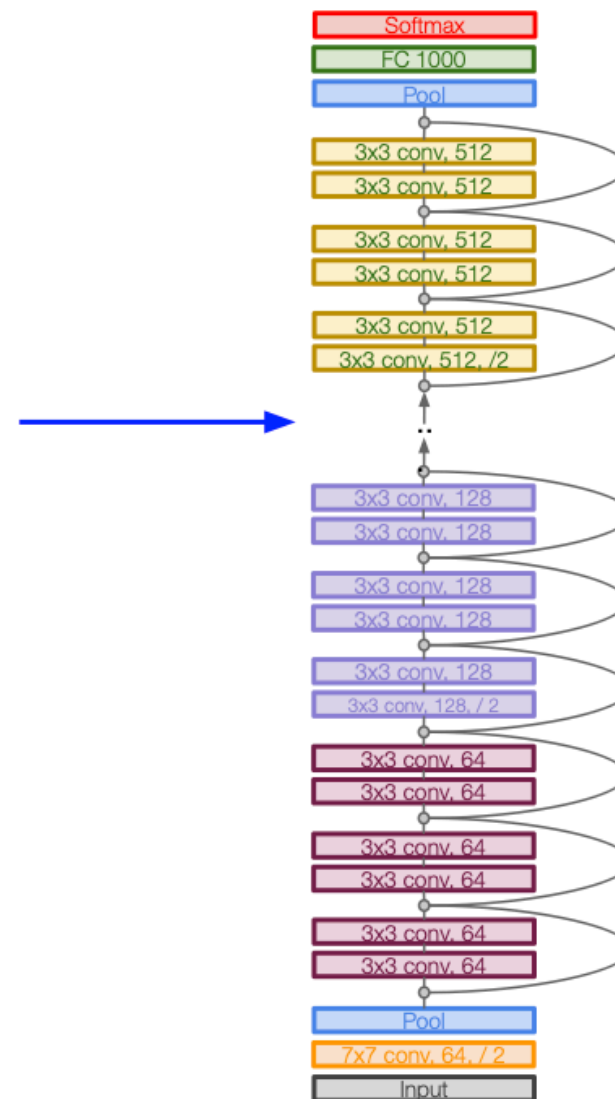
Full ResNet architecture:

- Stack residual blocks
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- Periodically, double # of filters and downsample spatially using stride 2 (/2 in each dimension)
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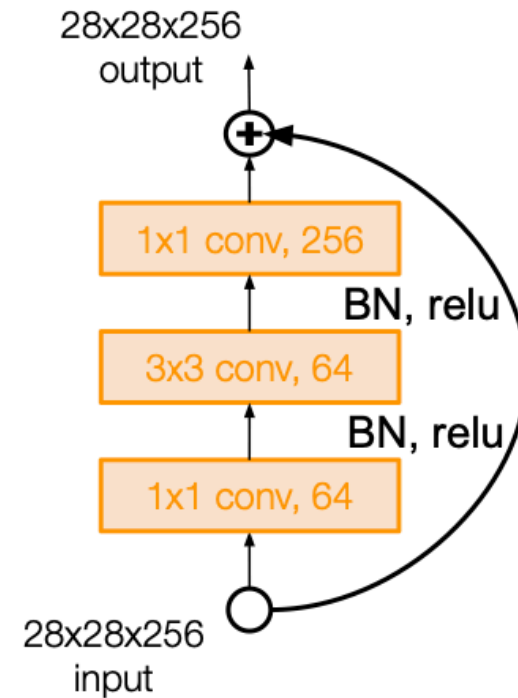
Total depths of 18, 34, 50,  
101, or 152 layers for  
ImageNet



# ResNet

*[He et al., 2015]*

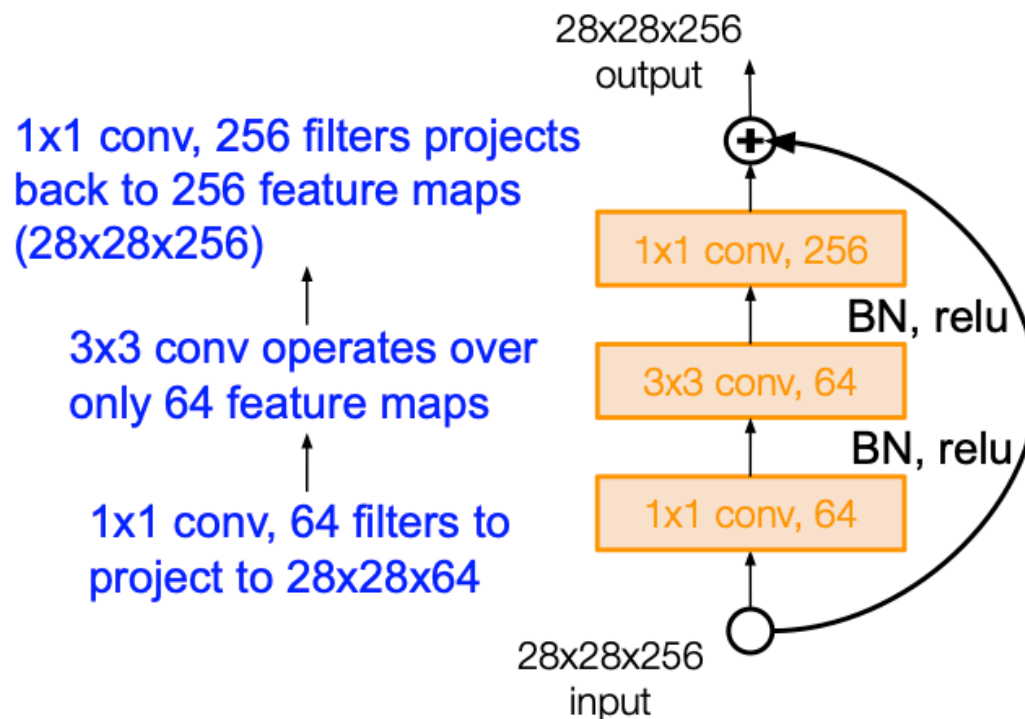
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# ResNet

Training ResNet in practice:

- Batch Normalization after every CONV layer
- Xavier initialization from He et al.
- SGD + Momentum (0.9)
- Learning rate: 0.1, divided by 10 when validation error plateaus
- Mini-batch size 256
- Weight decay of  $1e-5$
- No dropout used



# ResNet

## Experimental Results

- Able to train very deep networks without degrading (152 layers on ImageNet, 1202 on Cifar)
- Deeper networks now achieve lower training error as expected
- Swept 1st place in all ILSVRC and COCO 2015 competitions

## MSRA @ ILSVRC & COCO 2015 Competitions

- **1st places in all five main tracks**

- ImageNet Classification: “Ultra-deep” (quote Yann) **152-layer** nets
- ImageNet Detection: **16%** better than 2nd
- ImageNet Localization: **27%** better than 2nd
- COCO Detection: **11%** better than 2nd
- COCO Segmentation: **12%** better than 2nd

ILSVRC 2015 classification winner (3.6% top 5 error) -- better than “human performance”! (Russakovsky 2014)

# Beyond ResNet

- Squeeze-and-Excitation Network (SENet)
- Wide Residual Networks
- ResNeXt
- DenseNet

# VGGNet

Small filters, Deeper networks

8 layers (AlexNet)

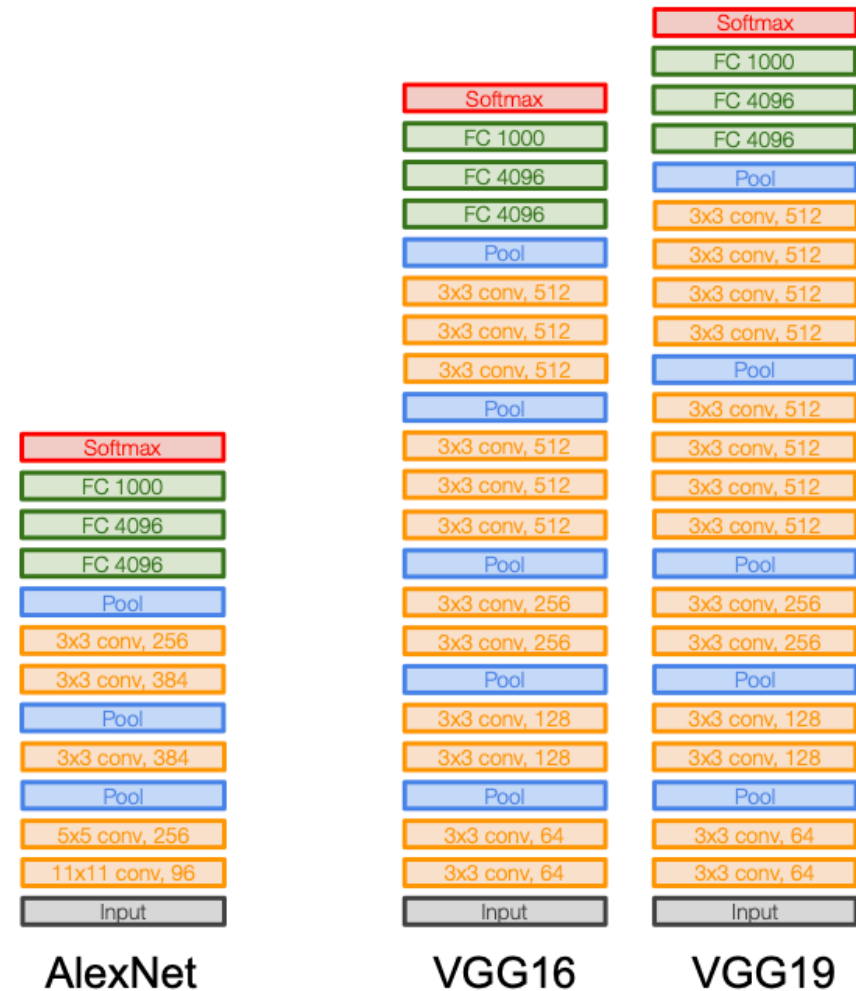
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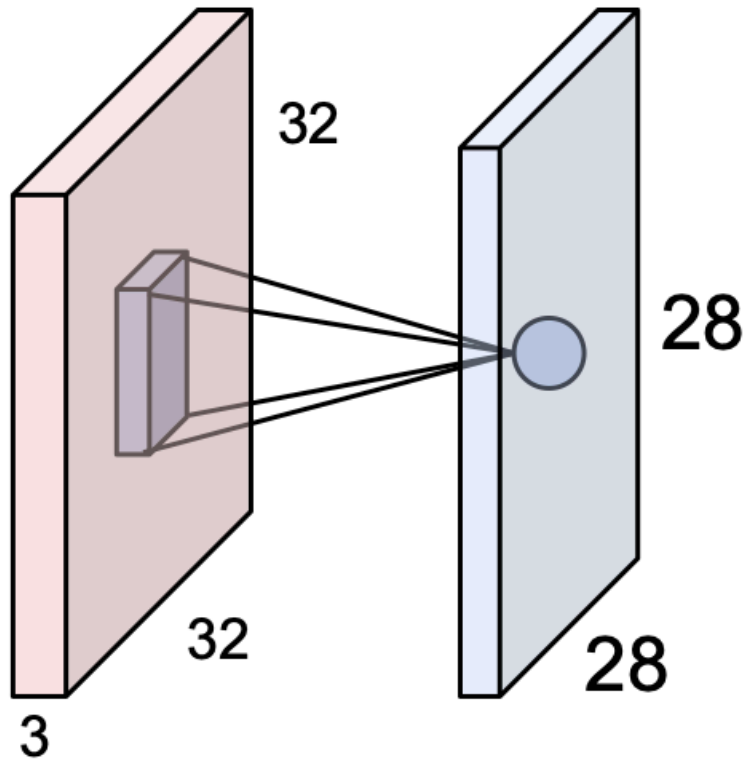
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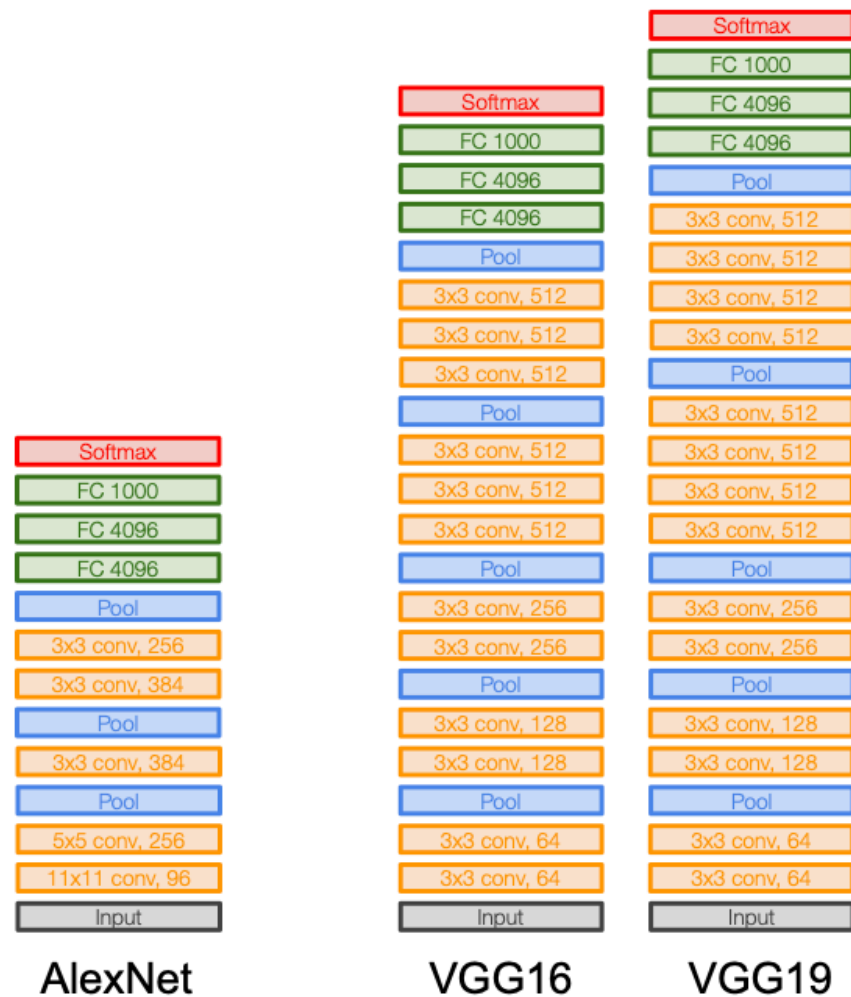
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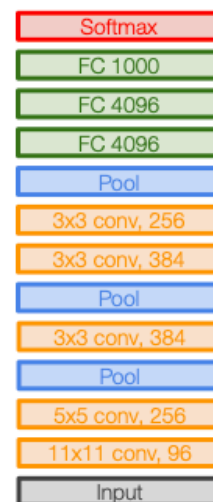
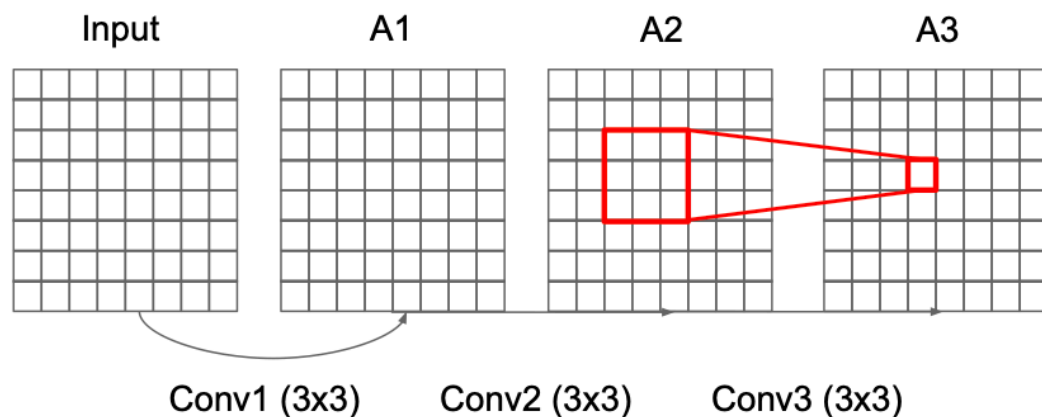
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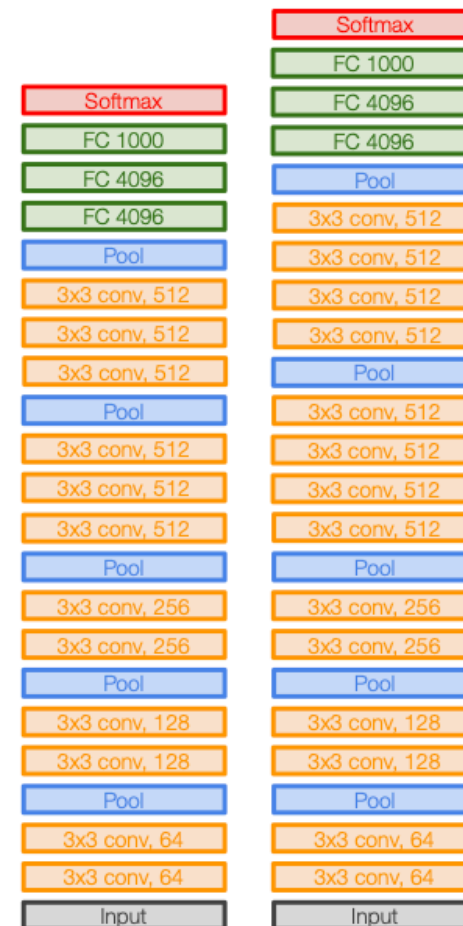


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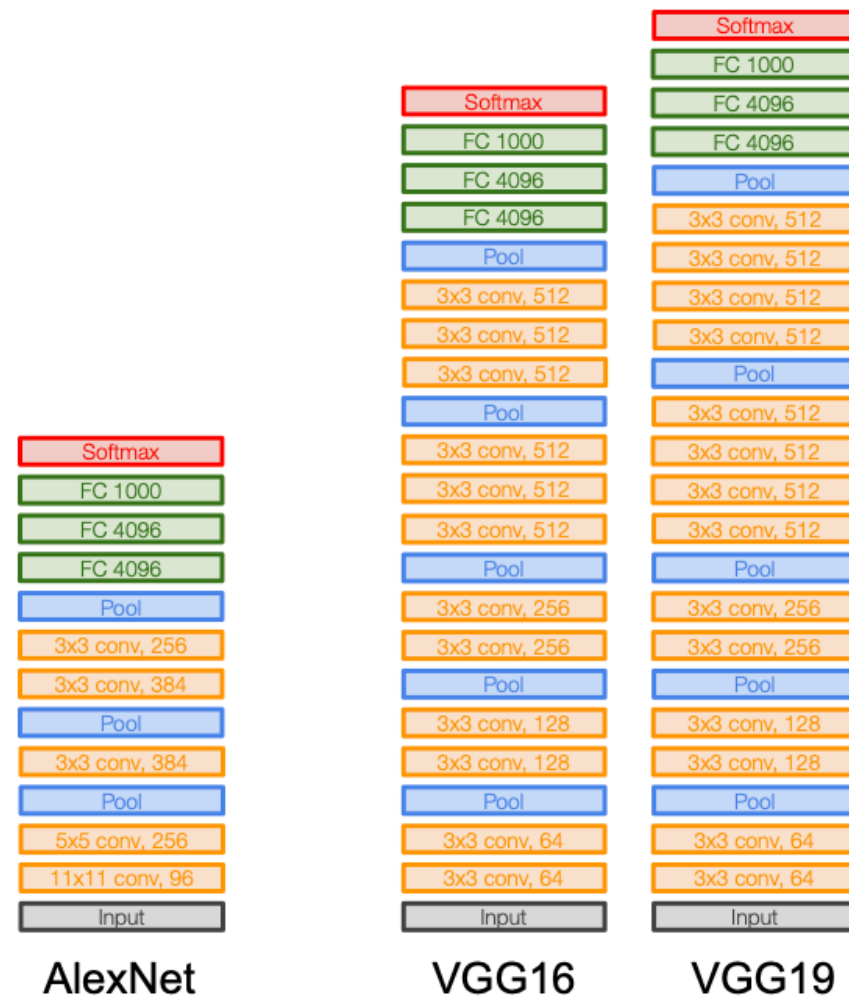
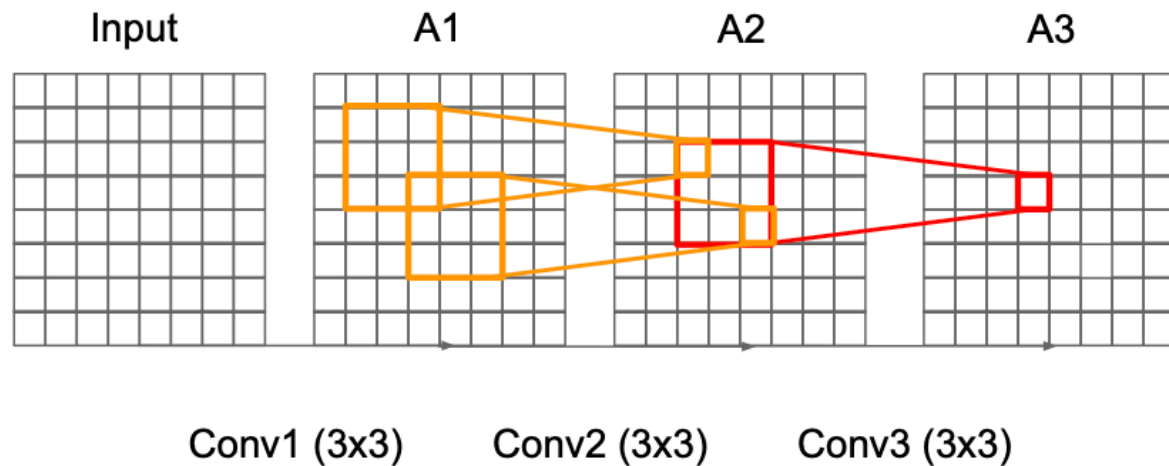


VGG16

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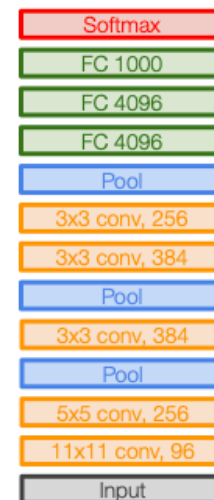
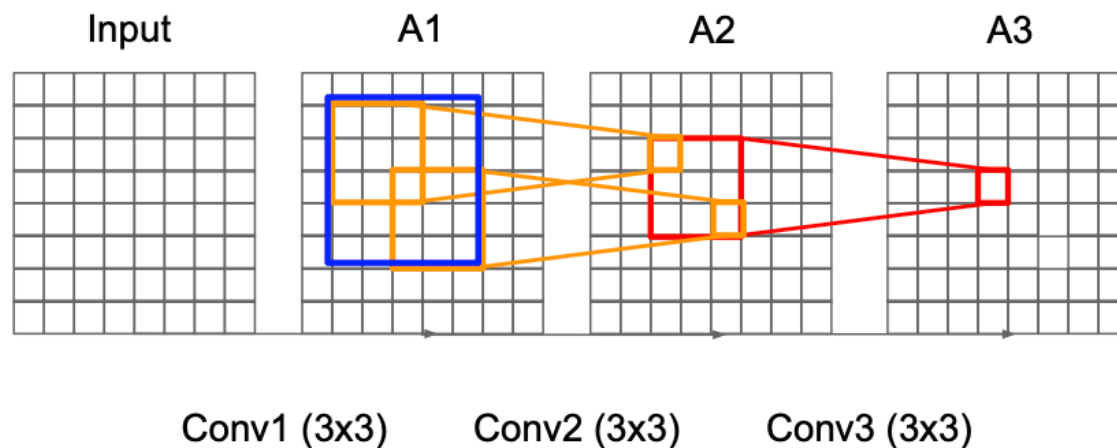
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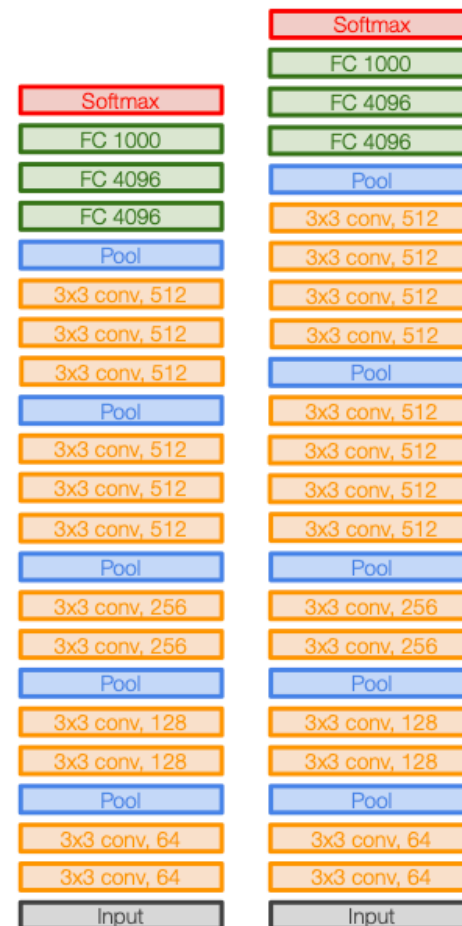


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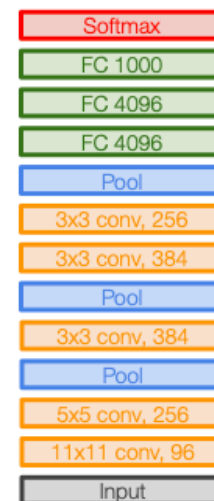
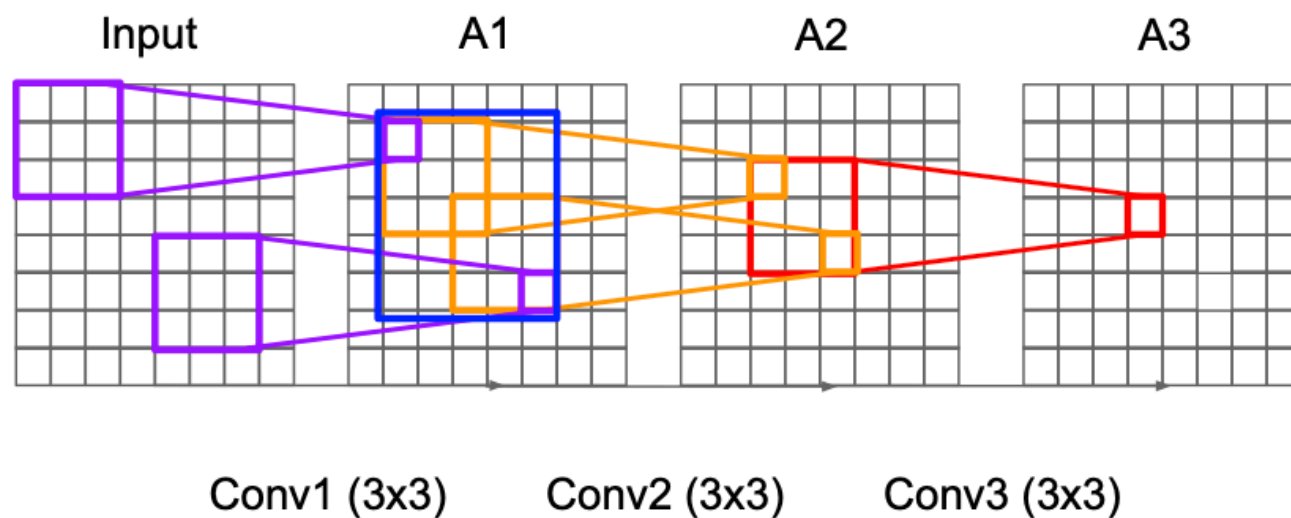
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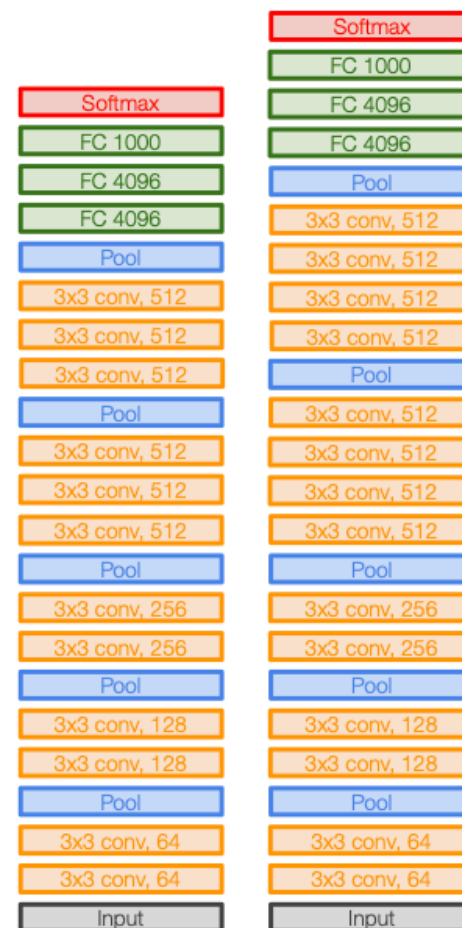


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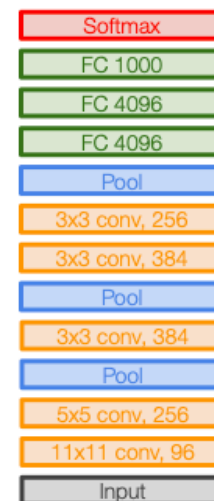
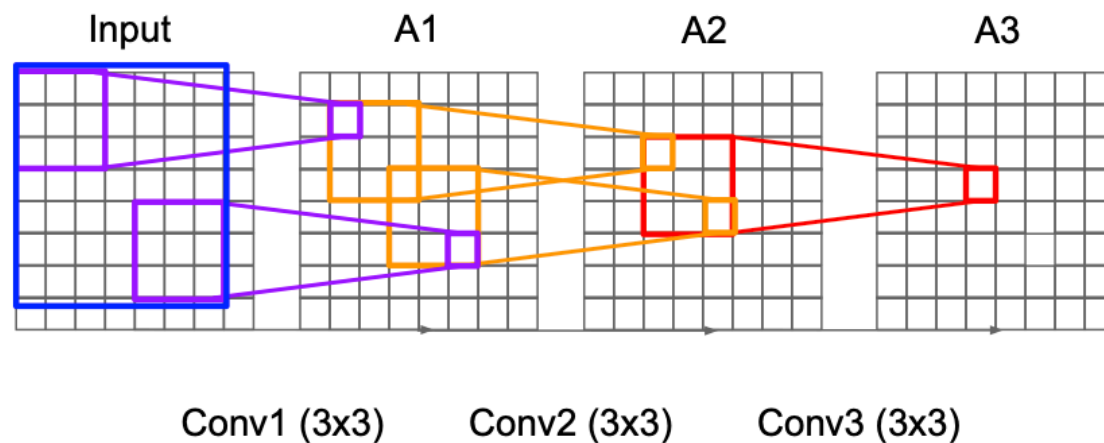


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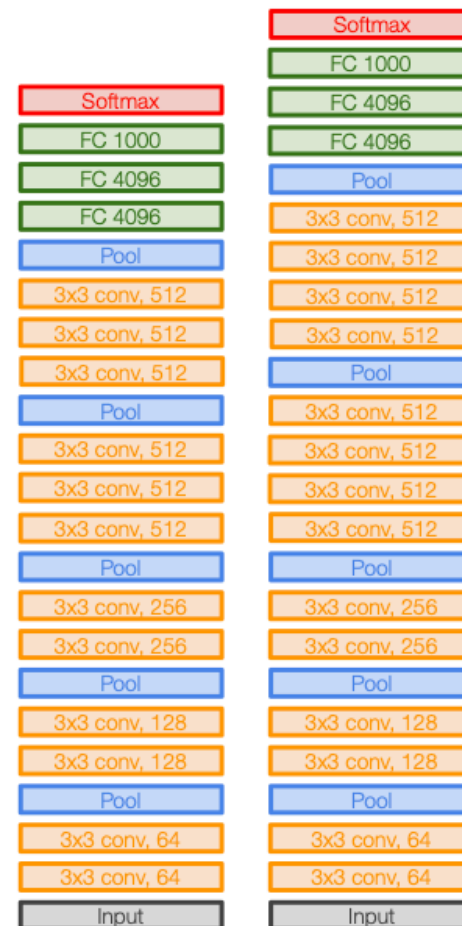
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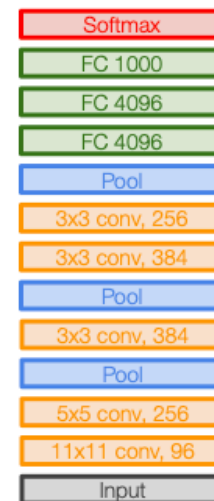
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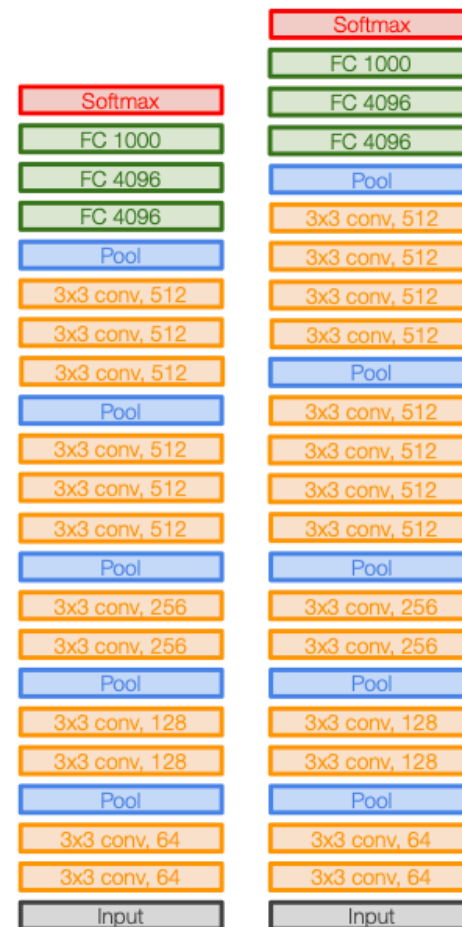
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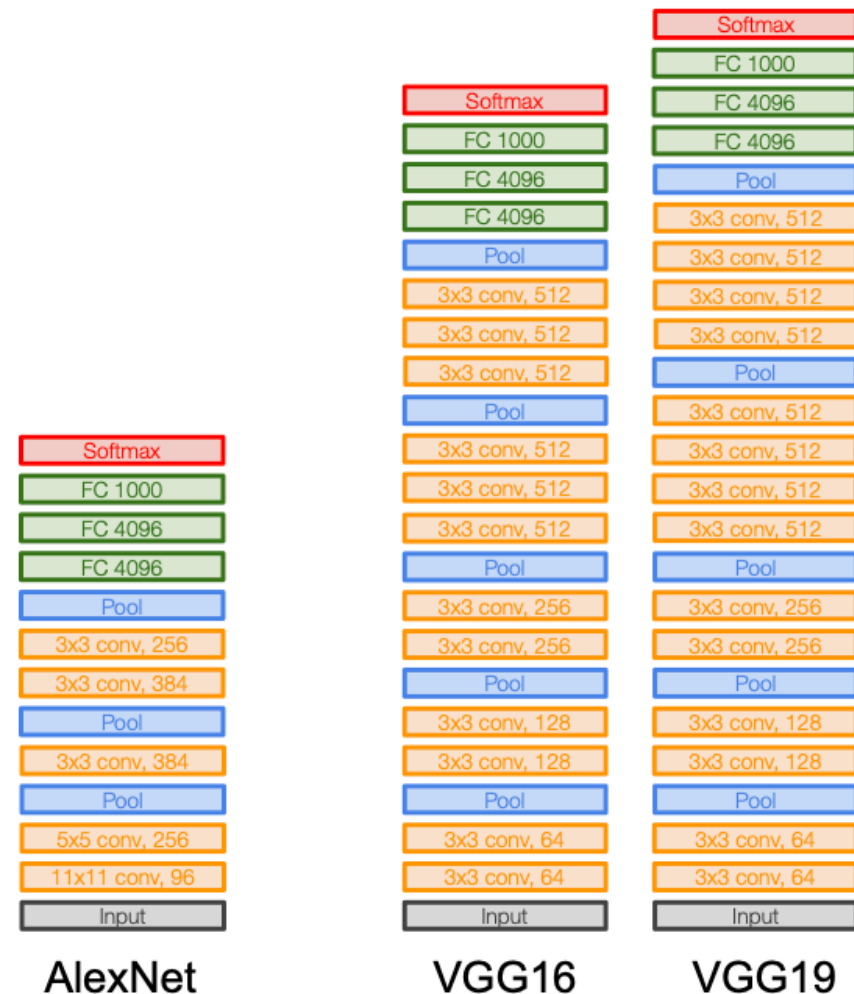
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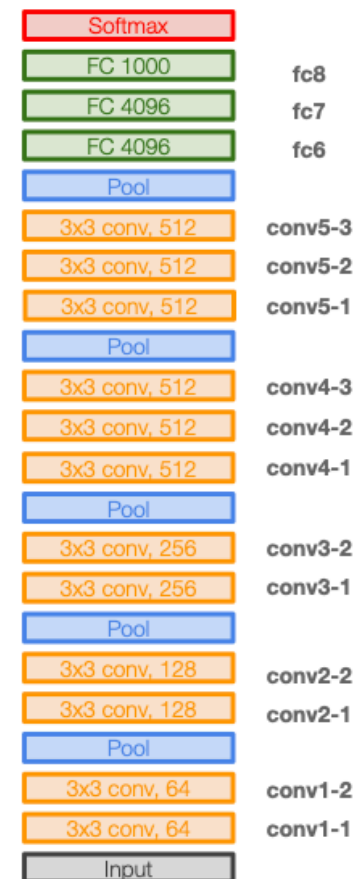
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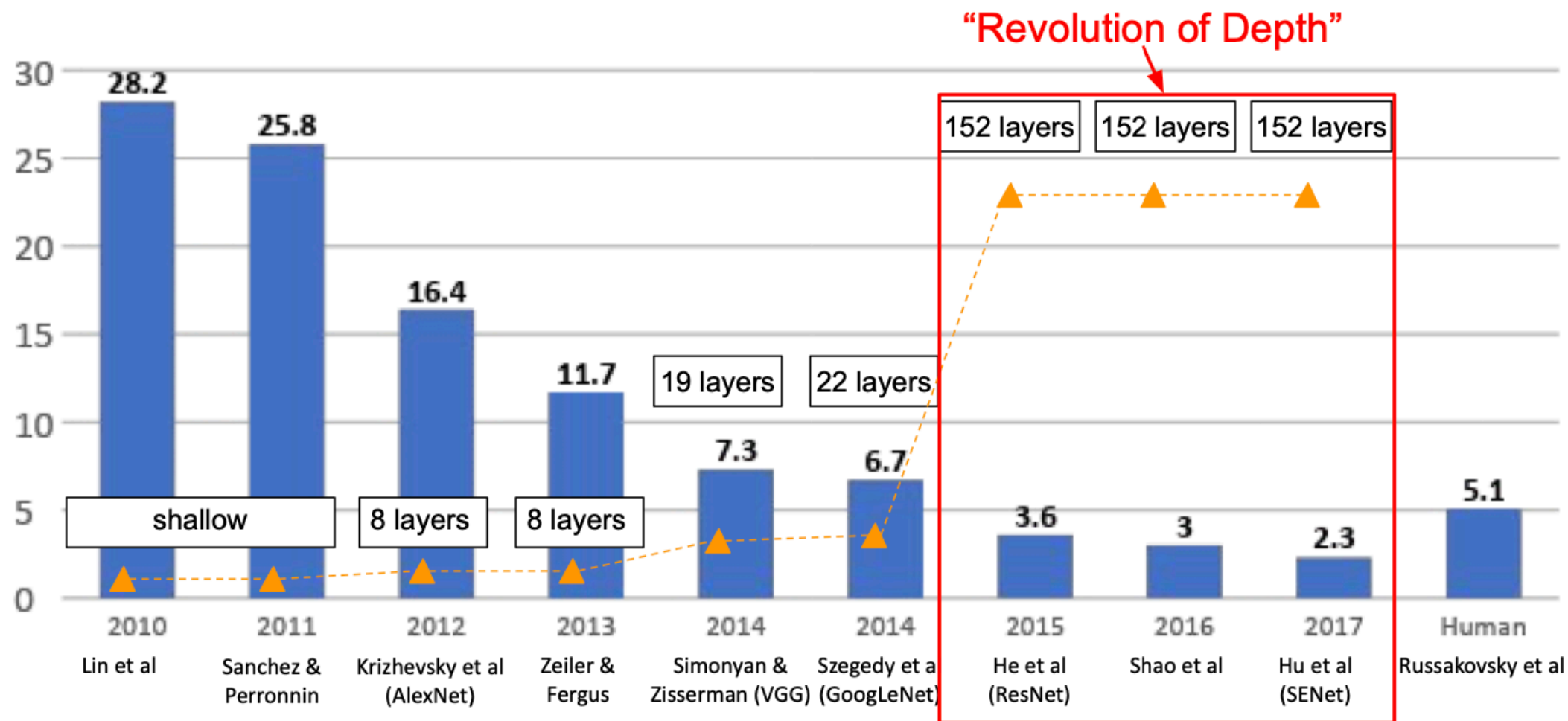
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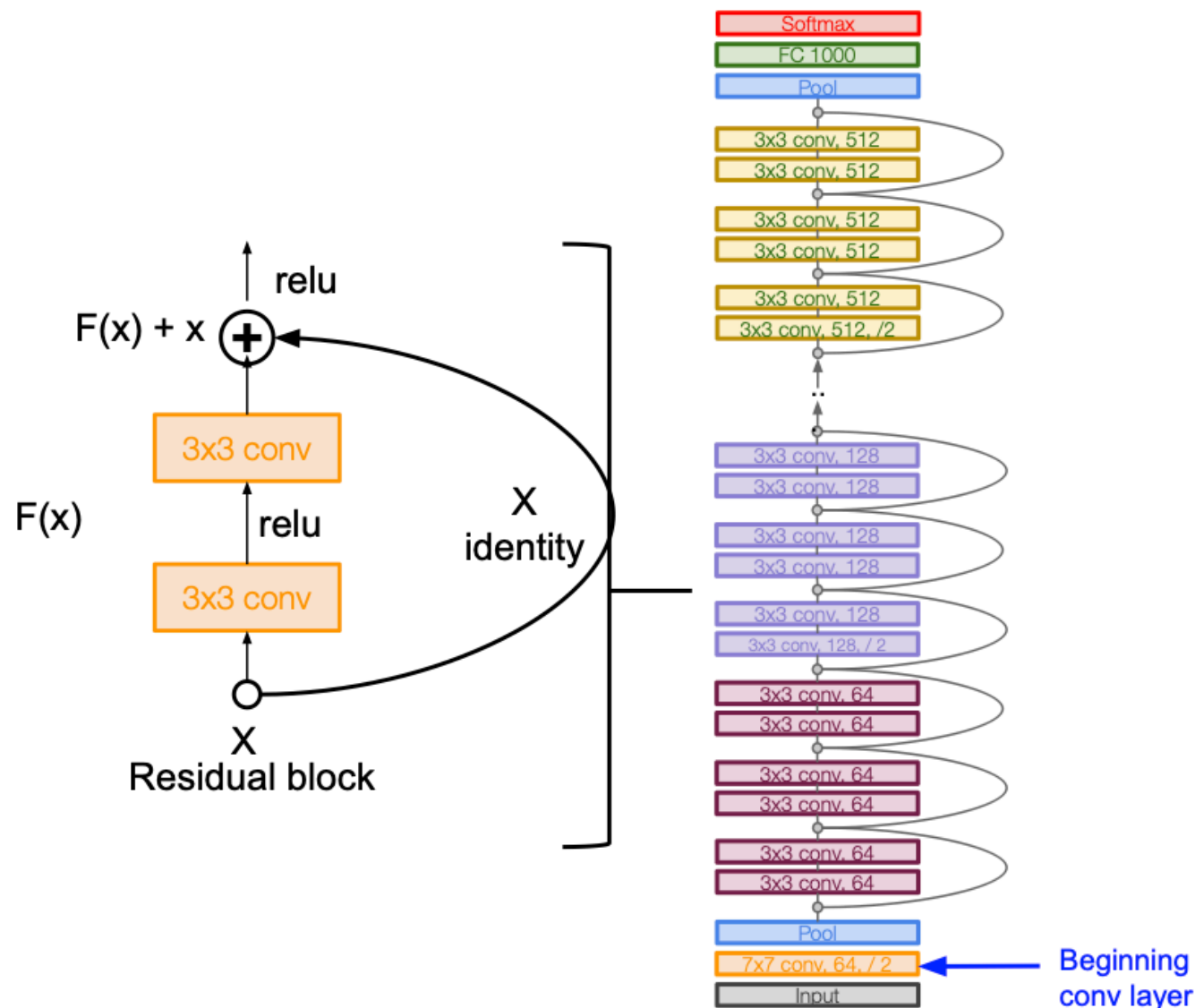
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Full ResNet architecture:

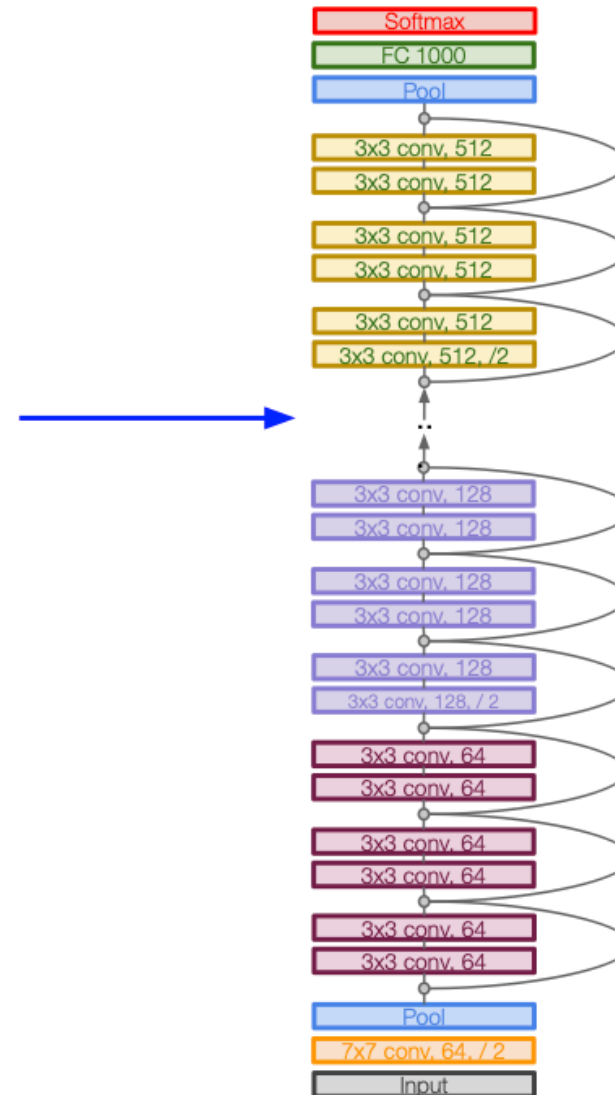
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# ResNet

Total depths of 18, 34, 50, 101, or 152 layers for ImageNet

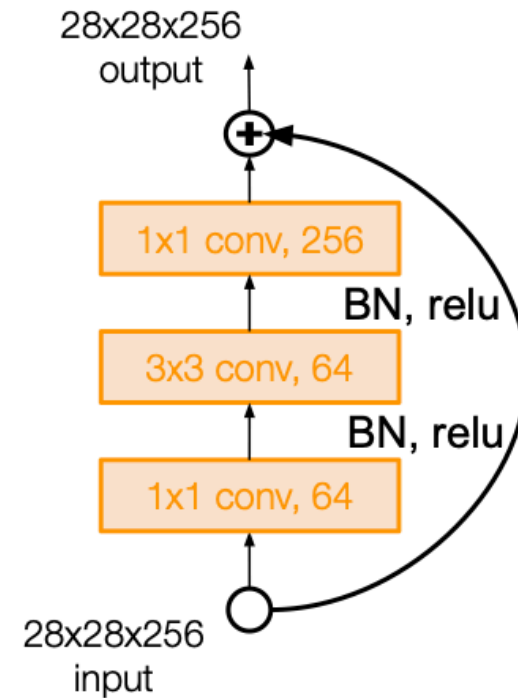




# ResNet

*[He et al., 2015]*

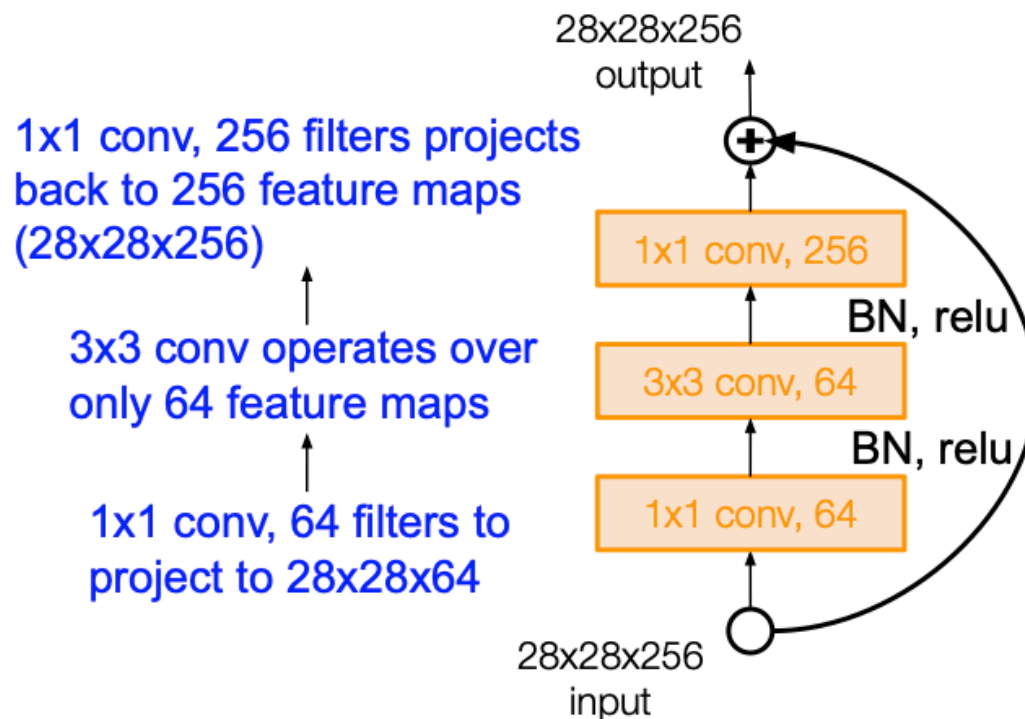
For deeper networks  
(ResNet-50+), use “bottleneck”  
layer to improve efficiency  
(similar to GoogLeNet)



# ResNet

[He et al., 2015]

For deeper networks  
(ResNet-50+), use “bottleneck”  
layer to improve efficiency  
(similar to GoogLeNet)



# ResNet

Training ResNet in practice:

- Batch Normalization after every CONV layer
- Xavier initialization from He et al.
- SGD + Momentum (0.9)
- Learning rate: 0.1, divided by 10 when validation error plateaus
- Mini-batch size 256
- Weight decay of  $1e-5$
- No dropout used

# ResNet

## Experimental Results

- Able to train very deep networks without degrading (152 layers on ImageNet, 1202 on Cifar)
- Deeper networks now achieve lower training error as expected
- Swept 1st place in all ILSVRC and COCO 2015 competitions

## MSRA @ ILSVRC & COCO 2015 Competitions

### • 1st places in all five main tracks

- ImageNet Classification: “Ultra-deep” (quote Yann) 152-layer nets
- ImageNet Detection: 16% better than 2nd
- ImageNet Localization: 27% better than 2nd
- COCO Detection: 11% better than 2nd
- COCO Segmentation: 12% better than 2nd

ILSVRC 2015 classification winner (3.6% top 5 error) -- better than “human performance”! (Russakovsky 2014)

# Beyond ResNet

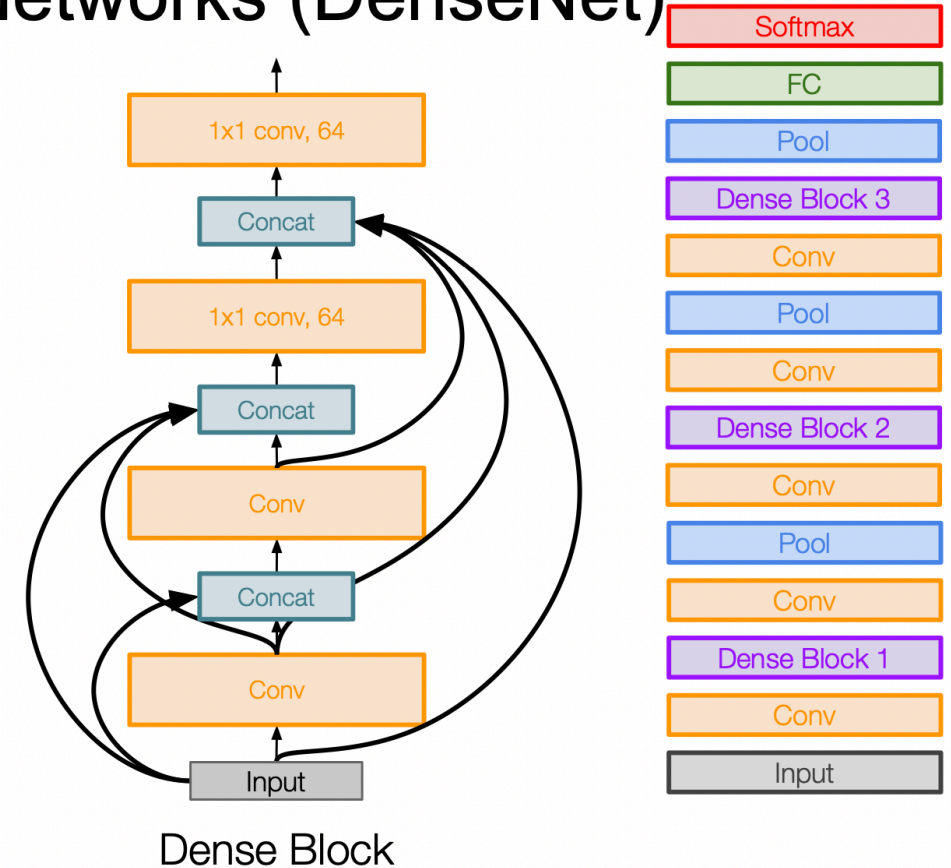
- Squeeze-and-Excitation Network (SENet)
- Wide Residual Networks
- ResNeXt
- DenseNet

# DenseNet

## Densely Connected Convolutional Networks (DenseNet)

[Huang et al. 2017]

- Dense blocks where each layer is connected to every other layer in feedforward fashion
- Alleviates vanishing gradient, strengthens feature propagation, encourages feature reuse
- Showed that shallow 50-layer network can outperform deeper 152 layer ResNet



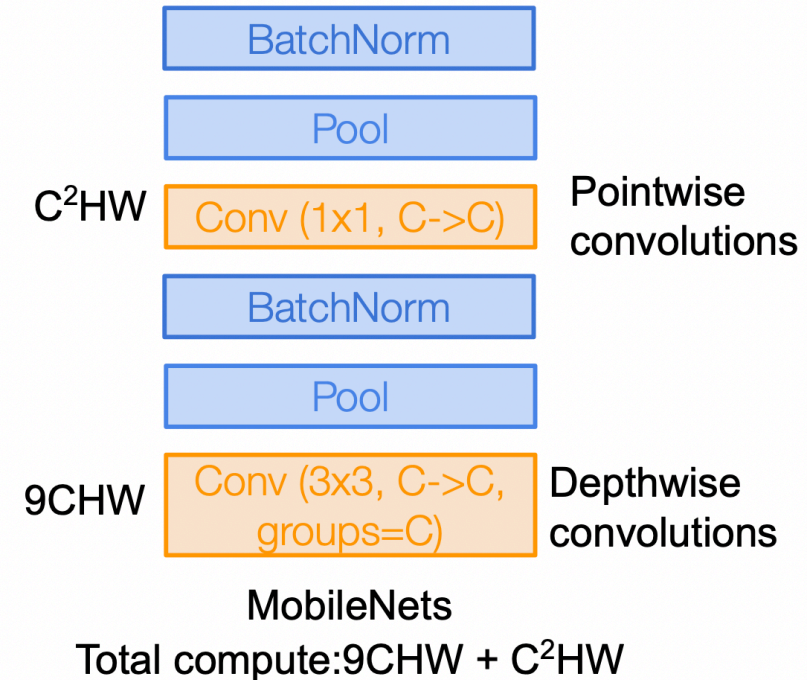
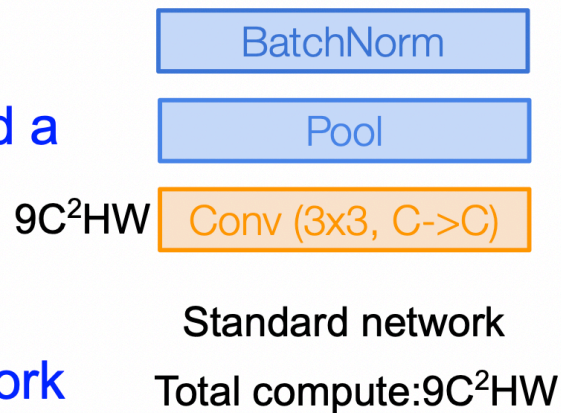
# Beyond ResNet

- Squeeze-and-Excitation Network (SENet)
  - Wide Residual Networks
  - ResNeXt
  - DenseNet
- 
- Attention-based networks: ViT, SwinTransformer
  - MLP-based networks
- 
- MobileNet → efficiency

# Efficient Networks

## MobileNets: Efficient Convolutional Neural Networks for Mobile Applications *[Howard et al. 2017]*

- Depthwise separable convolutions replace standard convolutions by factorizing them into a depthwise convolution and a  $1 \times 1$  convolution
- Much more efficient, with little loss in accuracy
- Follow-up MobileNetV2 work in 2018 (Sandler et al.)
- ShuffleNet: Zhang et al, CVPR 2018





# Beyond ResNet

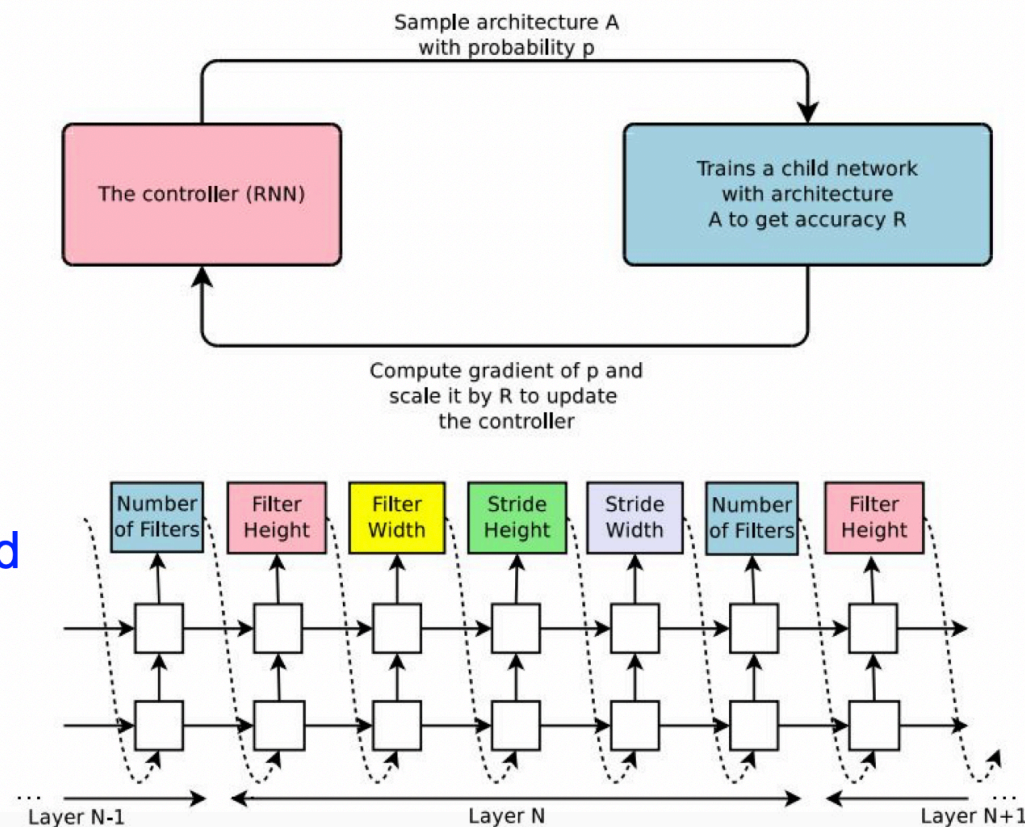
- Squeeze-and-Excitation Network (SENet)
- Wide Residual Networks
- ResNeXt
- DenseNet
  
- ViT, swinTransformer, MLP-based networks
  
- MobileNet → efficiency
  
- Neural architecture search

# Learning to Search for Network Architecture

## Neural Architecture Search with Reinforcement Learning (NAS)

[Zoph et al. 2016]

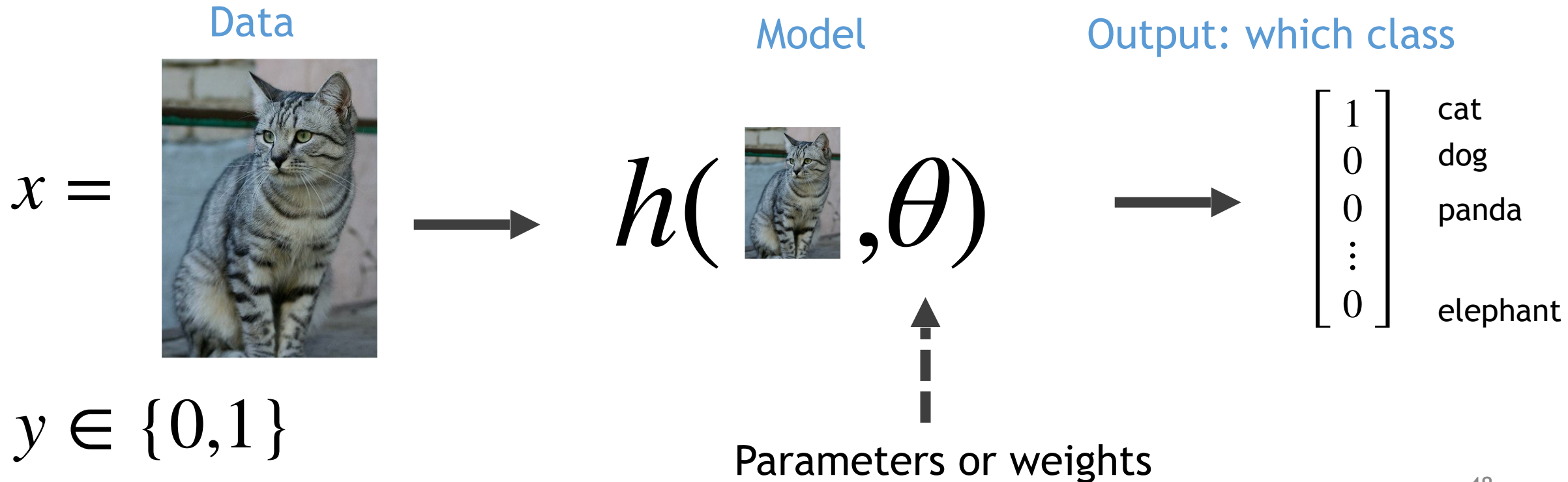
- “Controller” network that learns to design a good network architecture (output a string corresponding to network design)
- Iterate:
  - 1) Sample an architecture from search space
  - 2) Train the architecture to get a “reward”  $R$  corresponding to accuracy
  - 3) Compute gradient of sample probability, and scale by  $R$  to perform controller parameter update (i.e. increase likelihood of good architecture being sampled, decrease likelihood of bad architecture)



# Segmentation

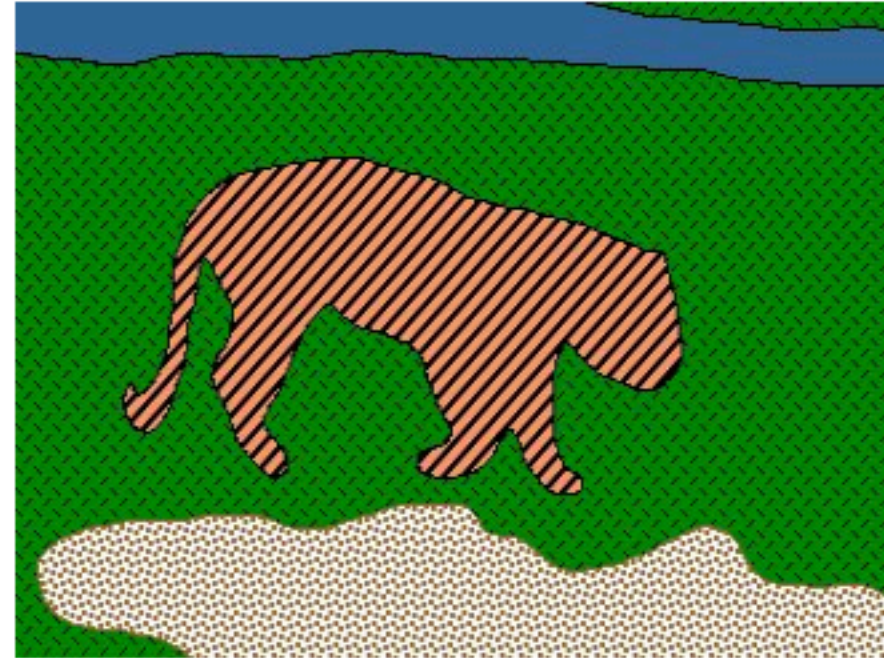
# Image Classification

- Classic definition: image classification is to categorize an image into several known classes (N).



# Image Segmentation

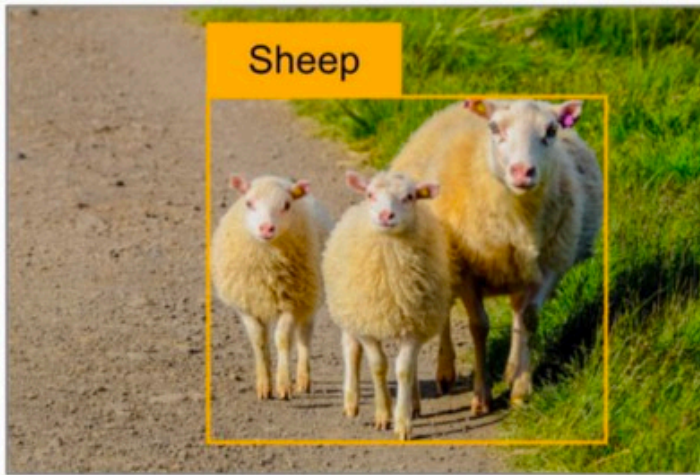
- Goal: identify groups of pixels that go together
  - Care about spatial extent
  - But not a global label



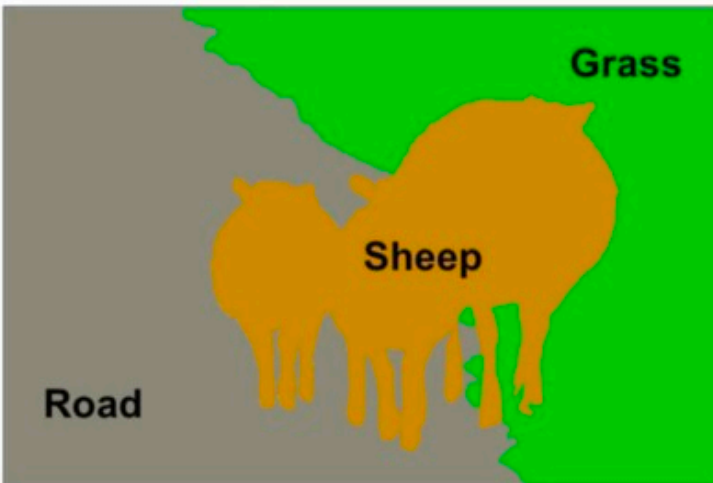
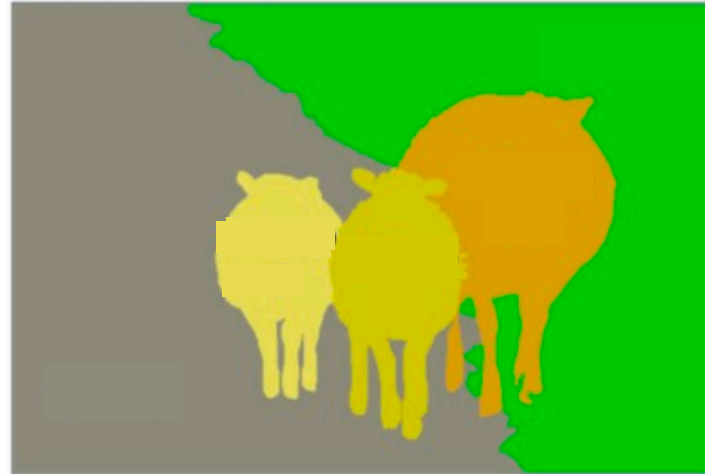


# We Care About Semantics

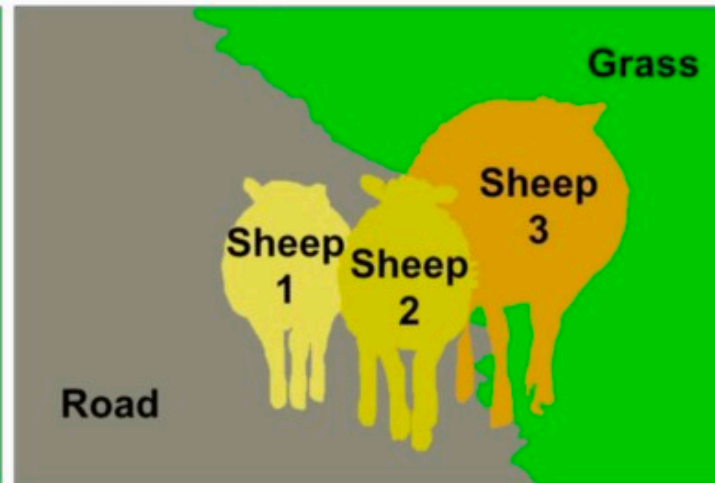
Classification + localization



Instance Segmentation



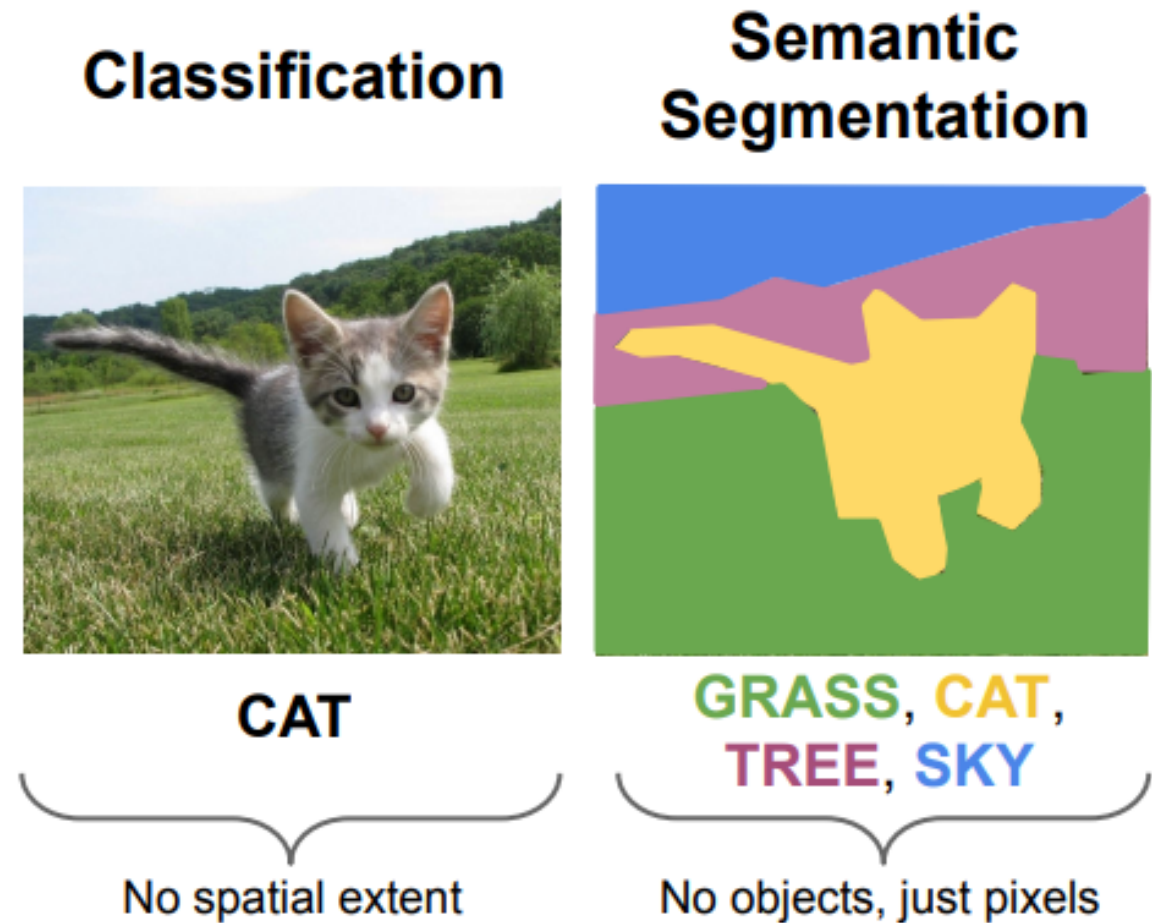
Semantic Segmentation



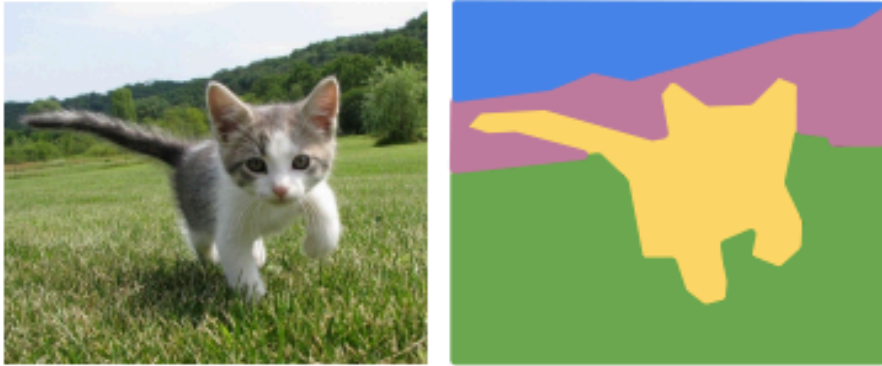
Semantic Instance Segmentation

# Semantic Segmentation

- Semantic segmentation is a dense labeling problem. Or, per-pixel classification problem.
- Sharing similar assumptions to classification: classes are pre-defined.

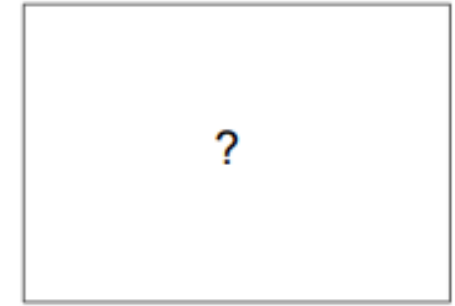


# Semantic Segmentation



GRASS, CAT,  
TREE, SKY, ...

Paired training data: for each training image, each pixel is labeled with a semantic category.



At test time, classify each pixel of a new image.

$$\mathcal{L}_{CE} = \text{mean}(H(P, Q)) = - \text{mean}\left(\sum_{x \in \mathcal{X}} P(x) \log Q(x)\right)$$



# Semantic Segmentation using Sliding Window

Full image

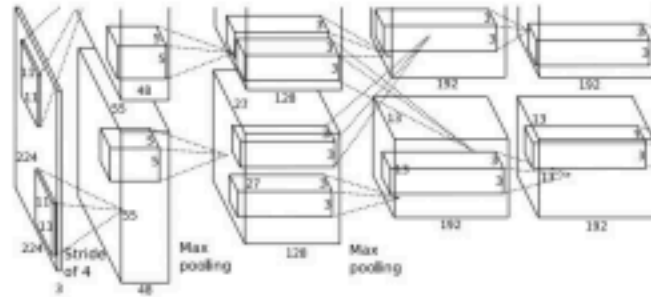


Impossible to classify without context

Q: how do we include context?

# Semantic Segmentation using CNN

Full image

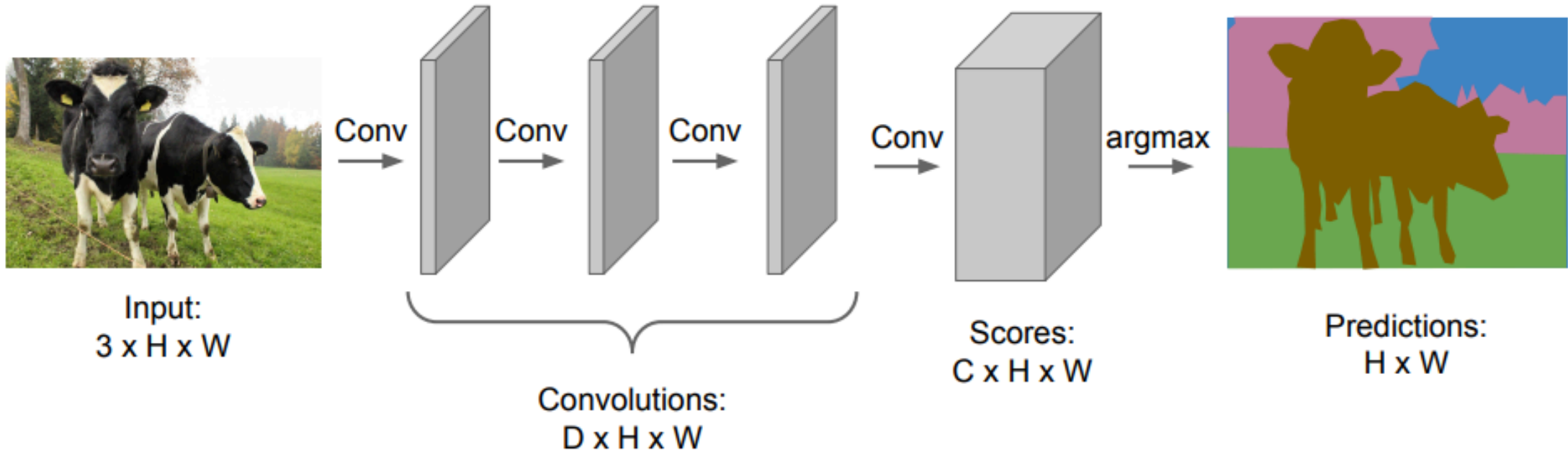


An intuitive idea: encode the entire image with conv net, and do semantic segmentation on top.

Problem: classification architectures often reduce feature spatial sizes to go deeper, but semantic segmentation requires the output size to be the same as input size.

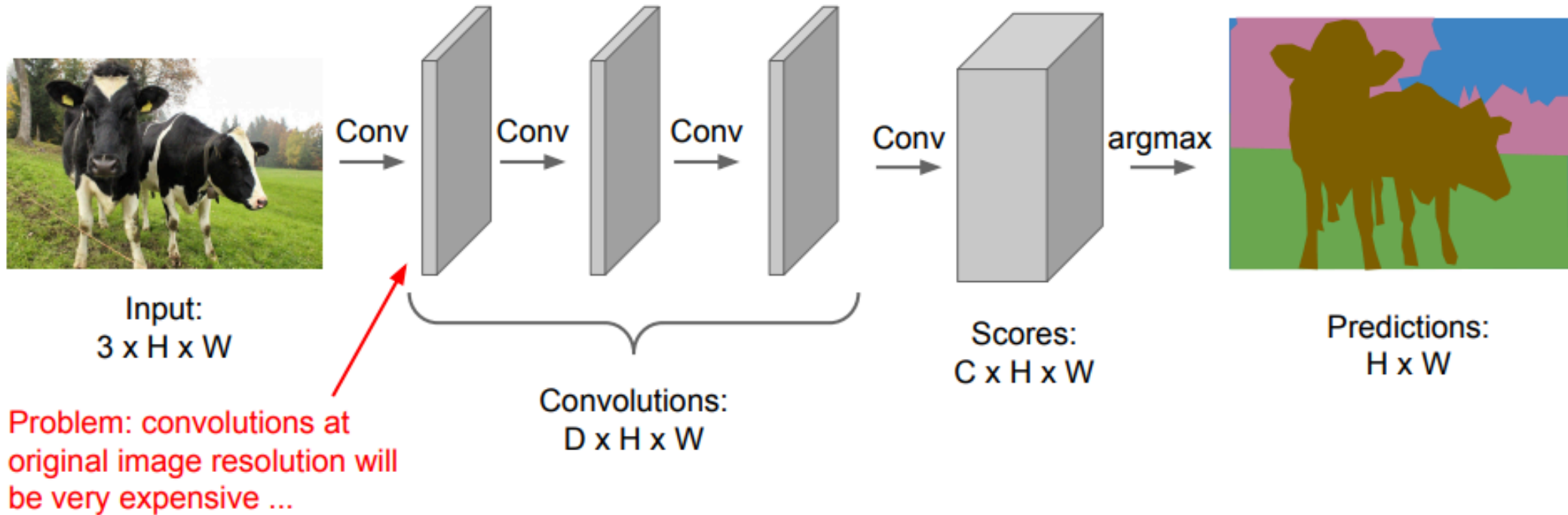
# Semantic Segmentation using Fully Convolution

Design a network with only convolutional layers without downsampling operators to make predictions for pixels all at once!



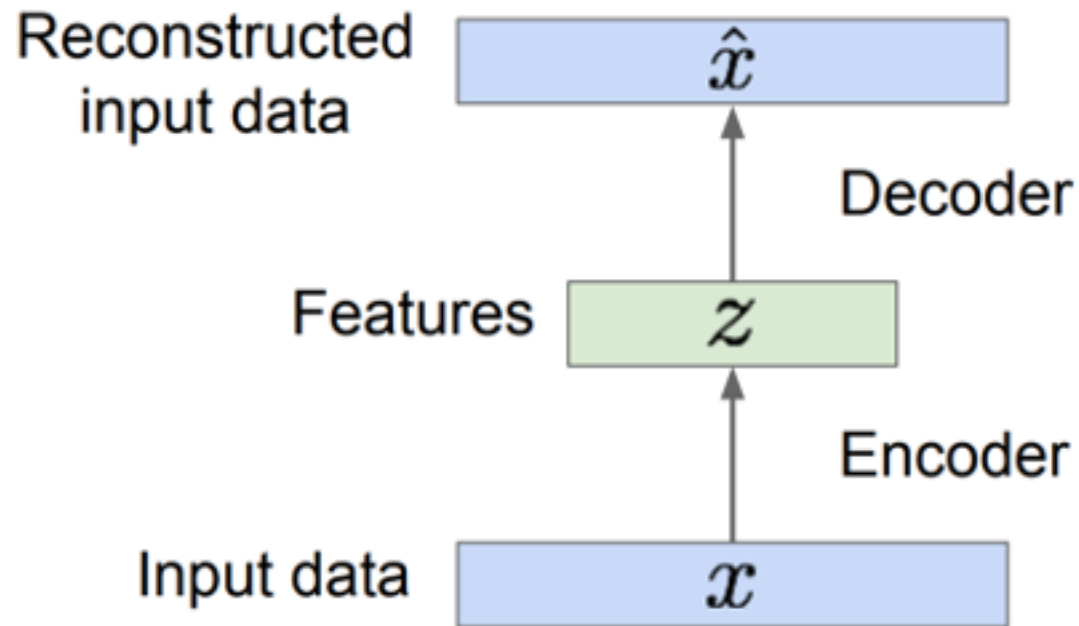
# Semantic Segmentation using Fully Convolution

Design a network with only convolutional layers without downsampling operators to make predictions for pixels all at once!



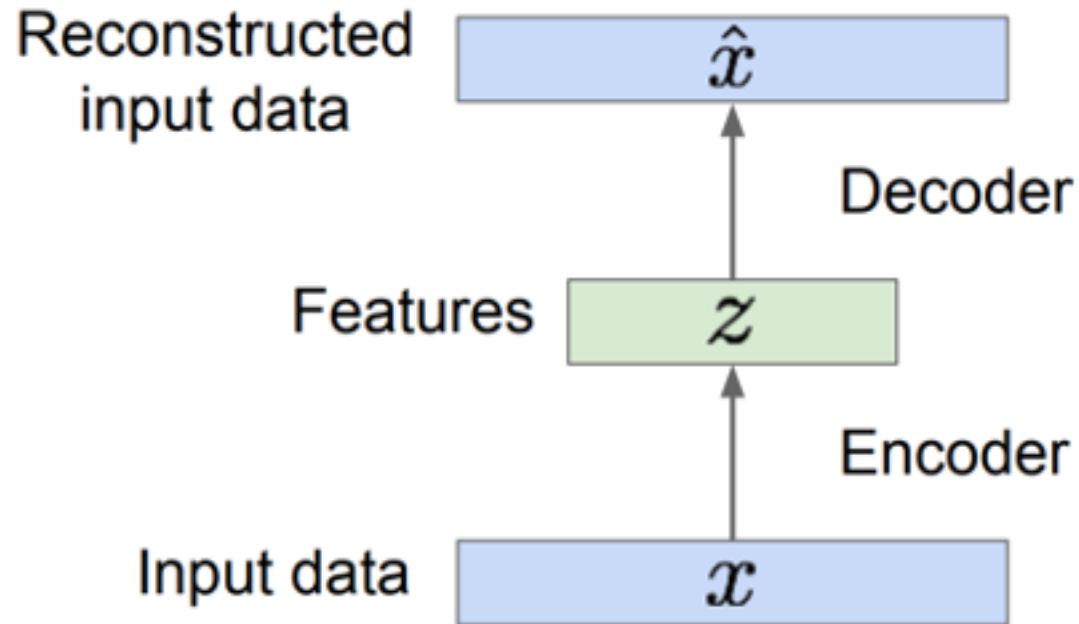
We need to reduce resolutions.

# Auto-Encoder



- AE encodes itself into a latent  $z$
- AE then decodes the latent  $z$  back to itself

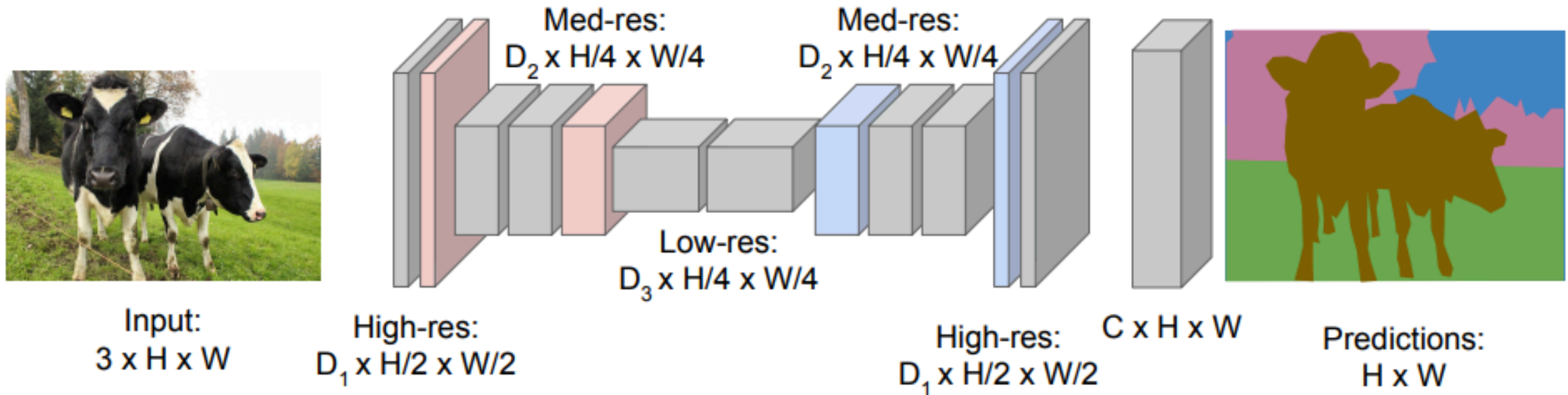
# Auto-Encoder



- Understanding AE
  - Information bottleneck: the dimension of  $z$  space is much smaller than that of  $x$
  - Get rid of redundant information via dimension reduction
  - The first step to all advanced segmentation networks

# Semantic Segmentation using Fully Convolution

Design network as a bunch of convolutional layers, with **downsampling** and **upsampling** inside the network!



Long, Shelhamer, and Darrell, "Fully Convolutional Networks for Semantic Segmentation", CVPR 2015  
Noh et al, "Learning Deconvolution Network for Semantic Segmentation", ICCV 2015



# Semantic Segmentation using Fully Convolution

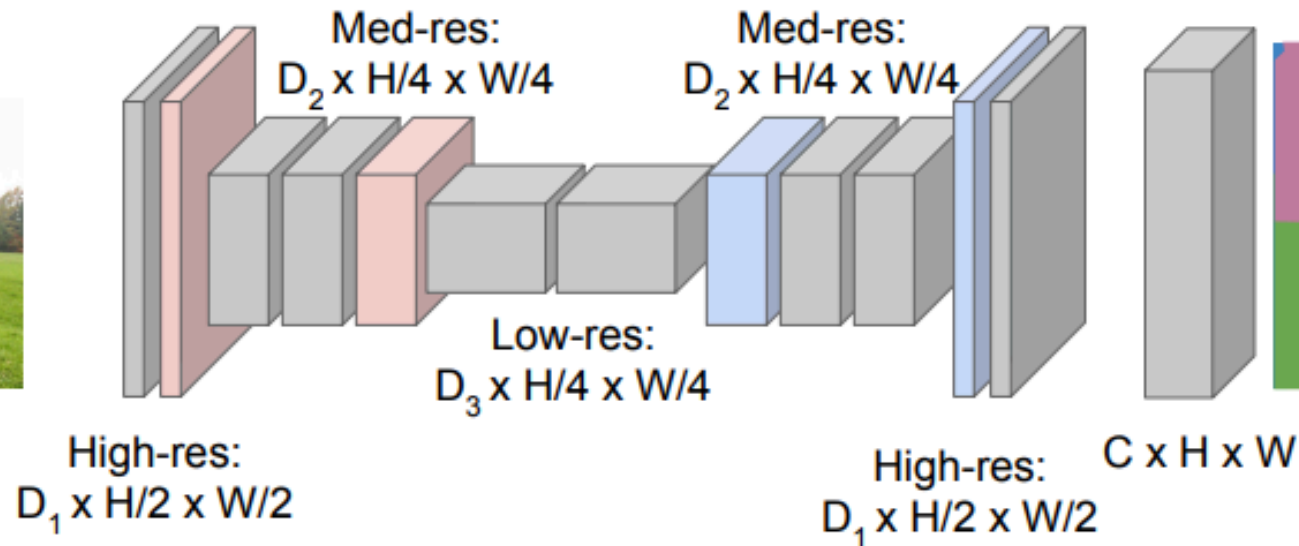
**Downsampling:**  
Pooling, strided  
convolution

Design network as a bunch of convolutional layers, with **downsampling** and **upsampling** inside the network!

**Upsampling:**  
???



Input:  
 $3 \times H \times W$



Predictions:  
 $H \times W$

Long, Shelhamer, and Darrell, "Fully Convolutional Networks for Semantic Segmentation", CVPR 2015  
Noh et al, "Learning Deconvolution Network for Semantic Segmentation", ICCV 2015



# In-Network Upsampling: Unpooling

**Nearest Neighbor**

1	2
3	4



1	1	2	2
1	1	2	2
3	3	4	4
3	3	4	4

Input: 2 x 2

Output: 4 x 4

**“Bed of Nails”**

1	2
3	4

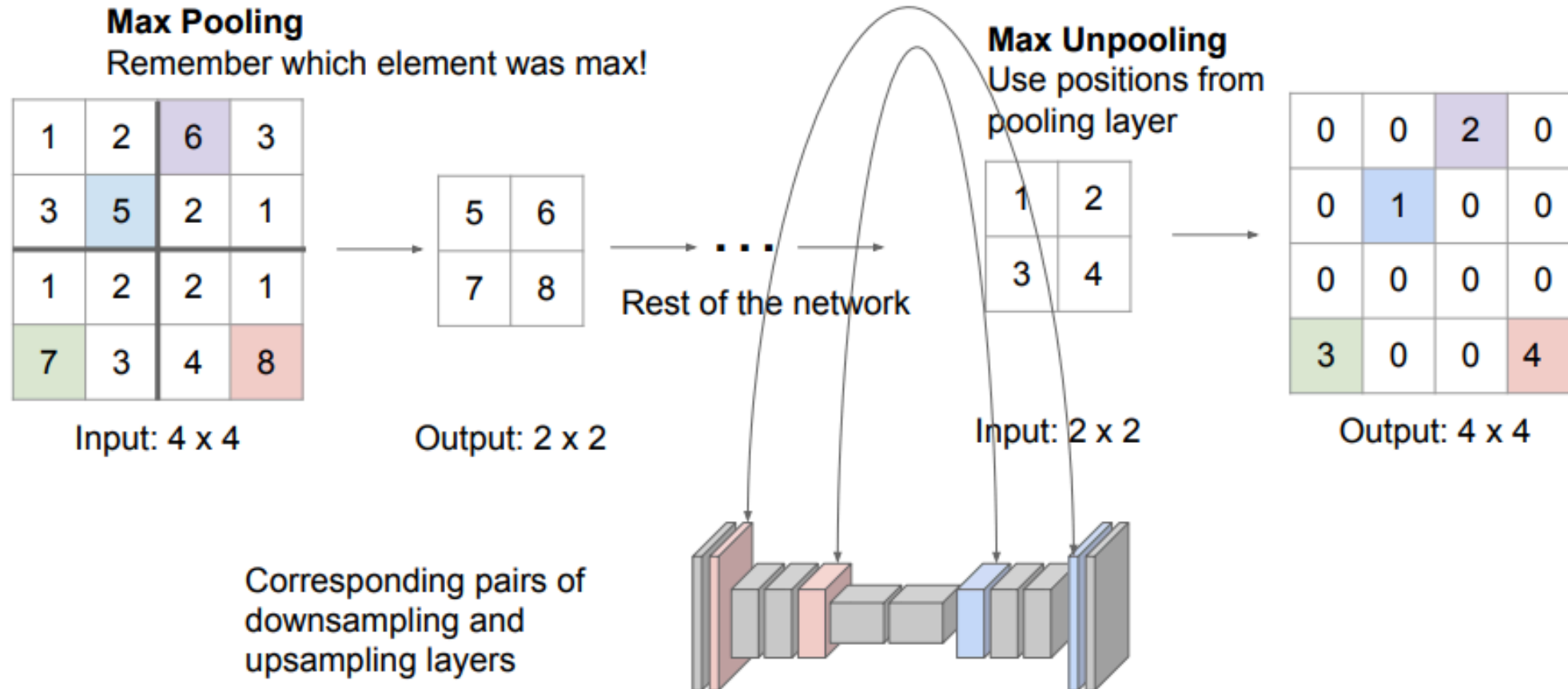


1	0	2	0
0	0	0	0
3	0	4	0
0	0	0	0

Input: 2 x 2

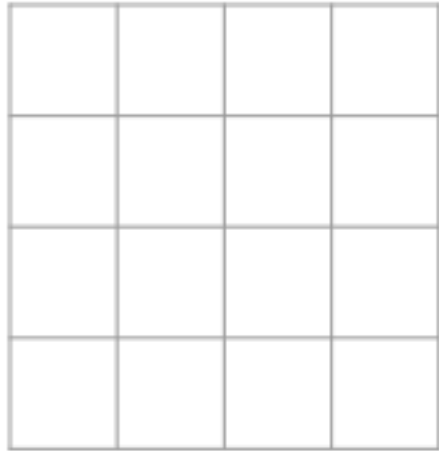
Output: 4 x 4

# In-Network Upsampling: Max Unpooling

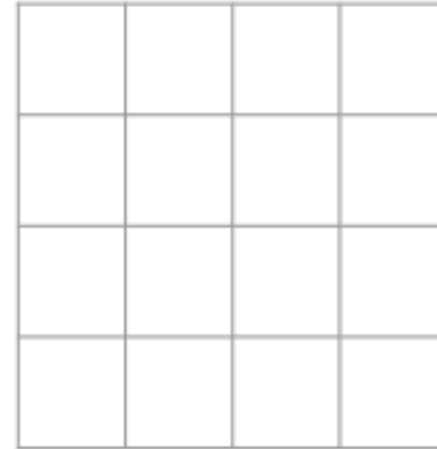


# Learnable Upsampling

**Recall:** Normal 3 x 3 convolution, stride 1 pad 1



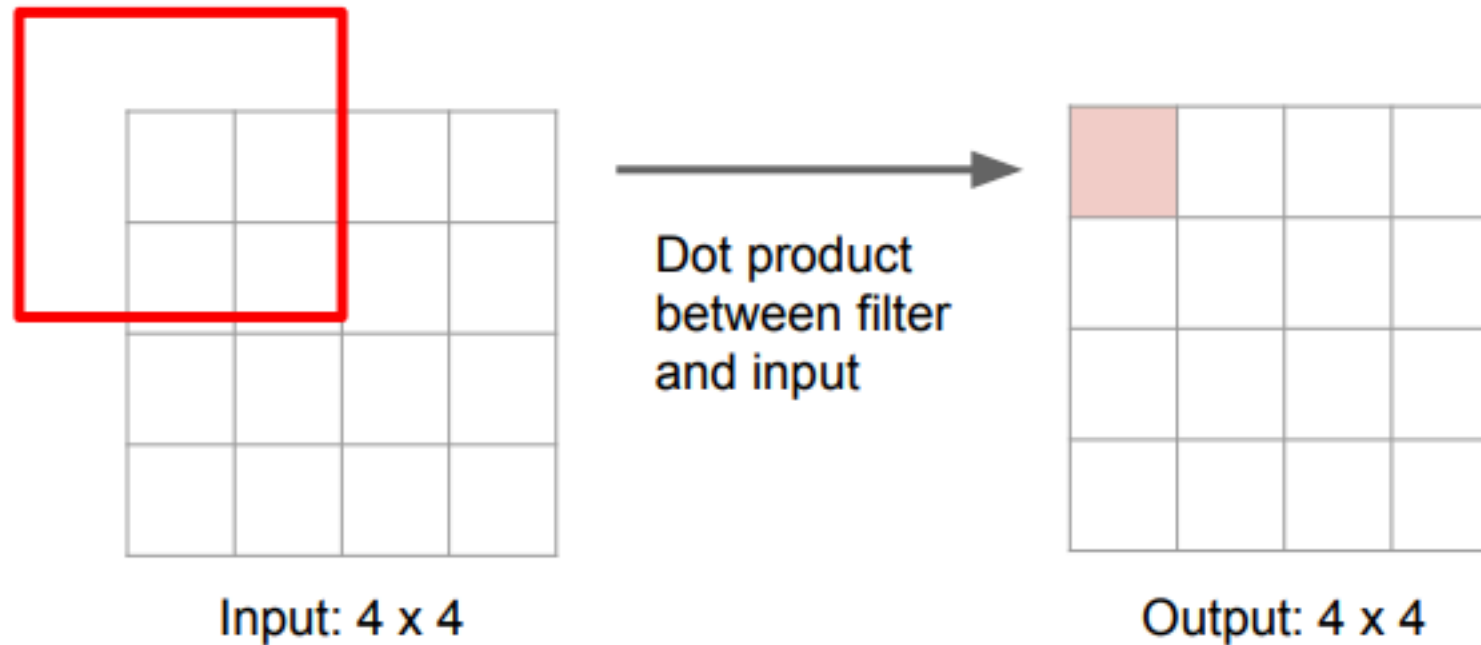
Input: 4 x 4



Output: 4 x 4

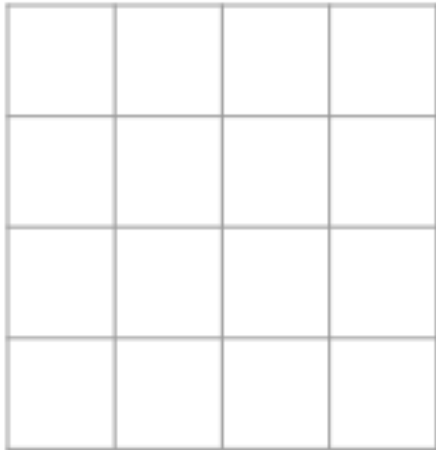
# Learnable Upsampling

**Recall:** Normal 3 x 3 convolution, stride 1 pad 1

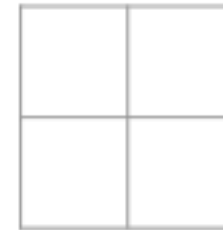


# Learnable Upsampling

**Recall:** Normal 3 x 3 convolution, stride 2 pad 1



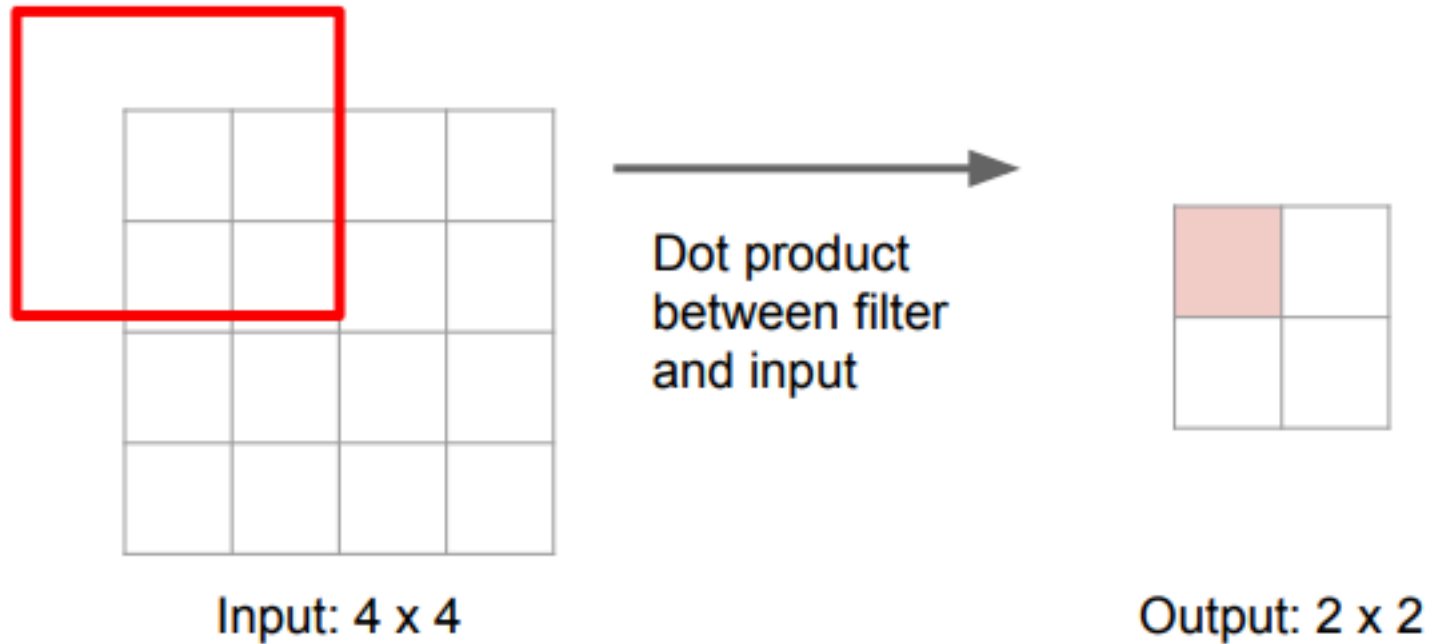
Input: 4 x 4



Output: 2 x 2

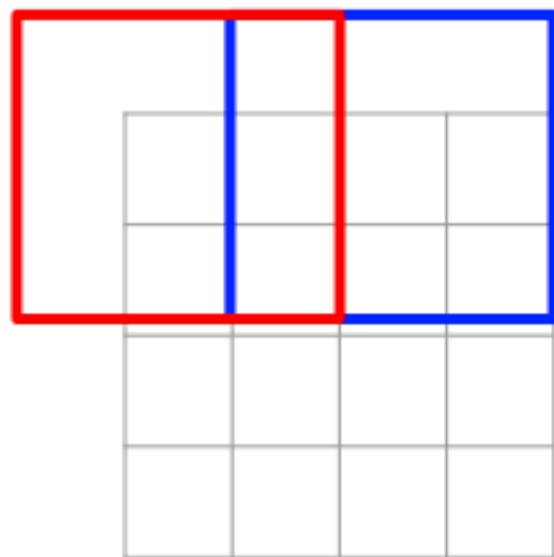
# Learnable Upsampling

**Recall:** Normal 3 x 3 convolution, stride 2 pad 1



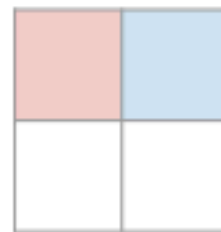
# Learnable Upsampling

**Recall:** Normal 3 x 3 convolution, stride 2 pad 1



Input: 4 x 4

Dot product  
between filter  
and input



Output: 2 x 2

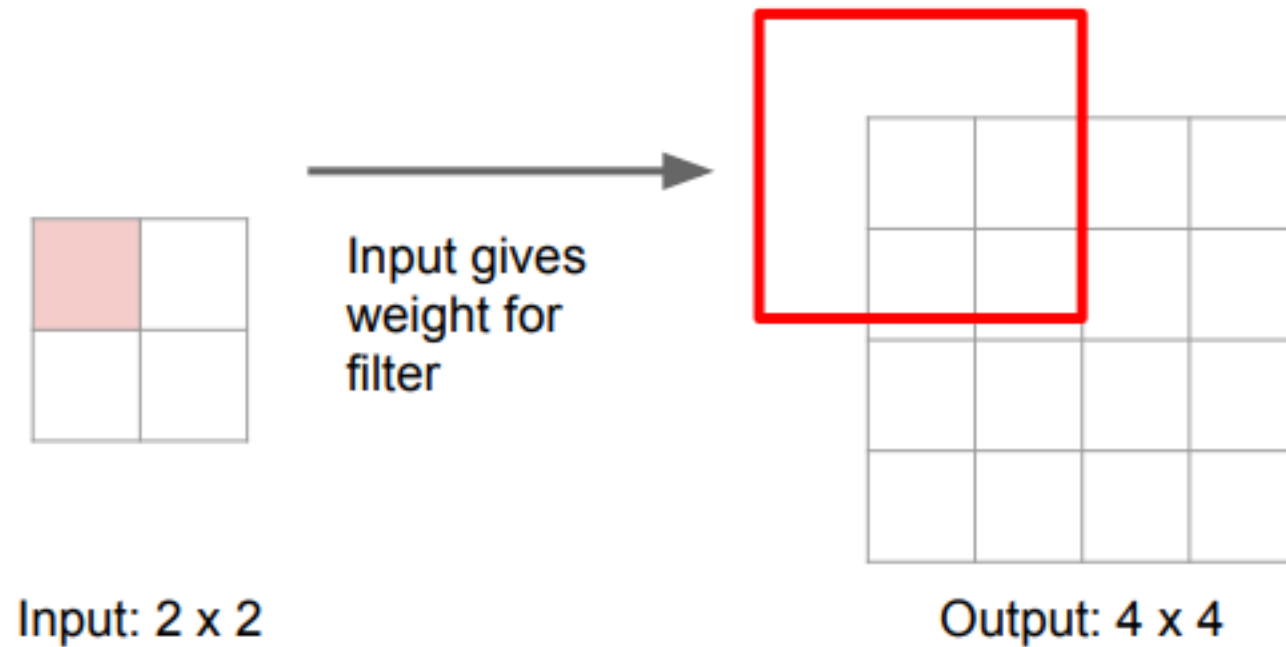
Filter moves 2 pixels in the input for every one pixel in the output

Stride gives ratio between movement in input and output

We can interpret strided convolution as “learnable downsampling”.

# Learnable Upsampling: Transposed Convolution

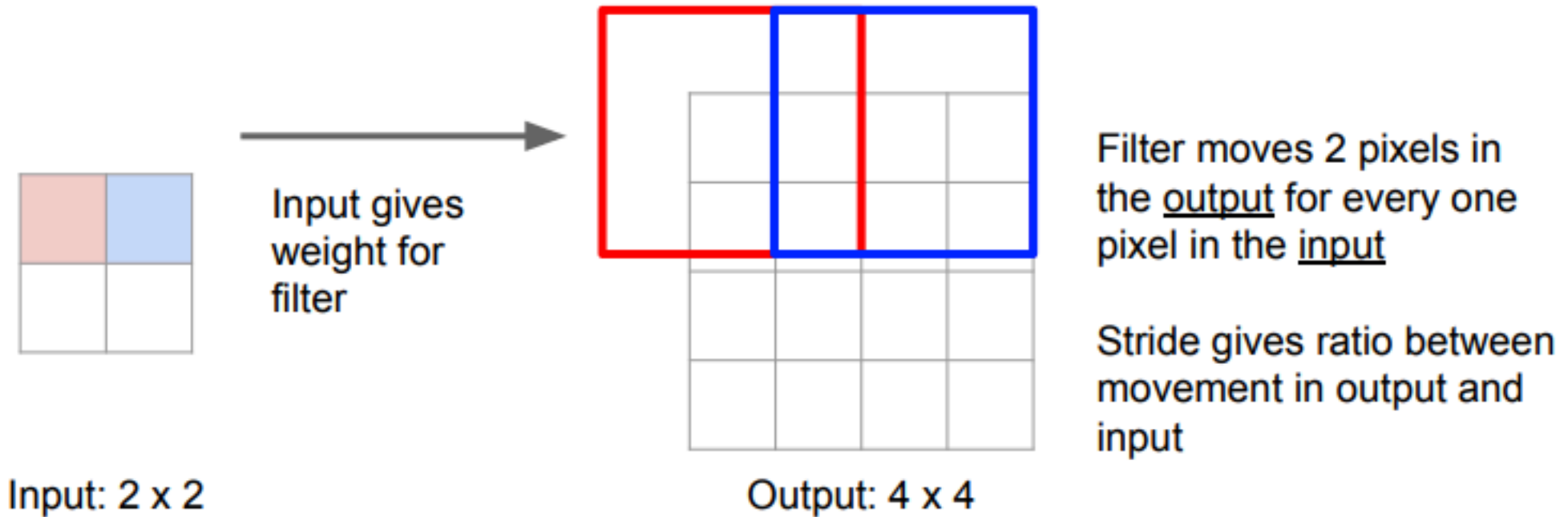
3 x 3 **transpose** convolution, stride 2 pad 1



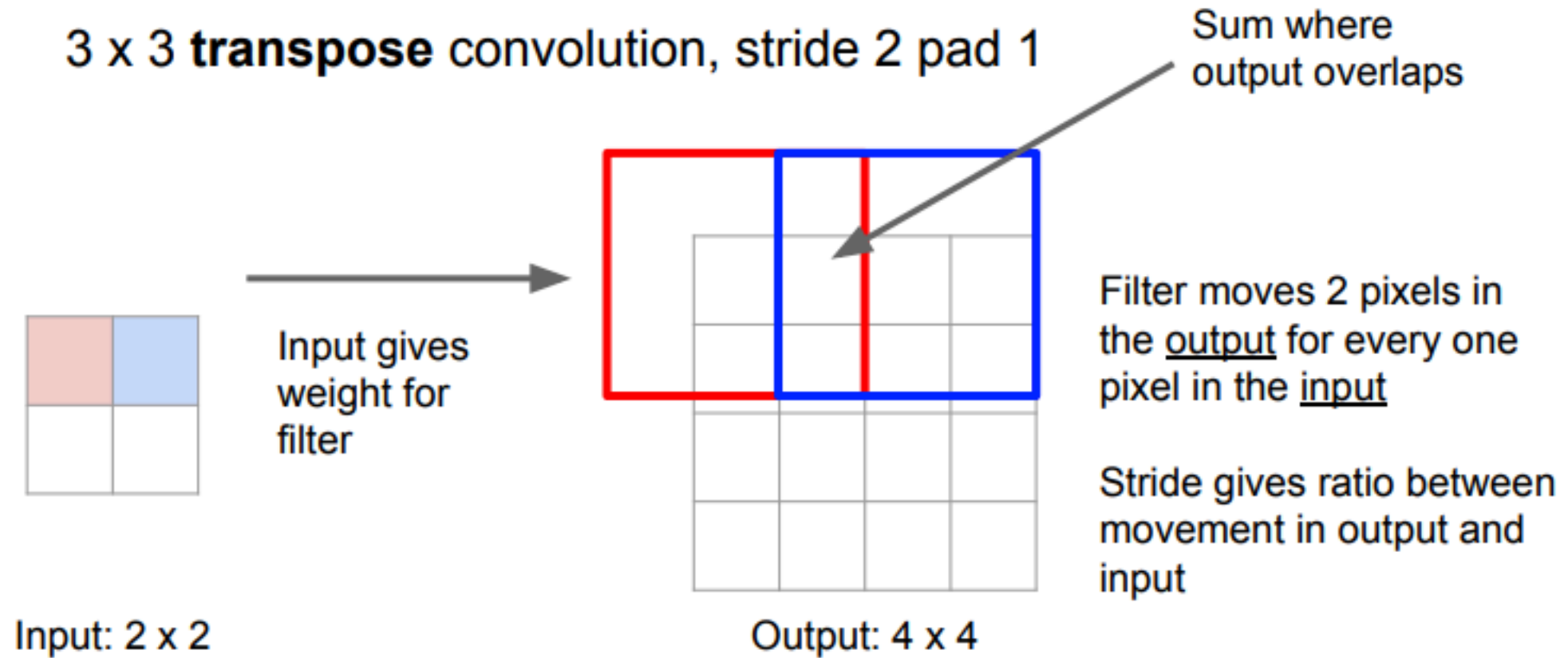


# Learnable Upsampling: Transposed Convolution

3 x 3 **transpose** convolution, stride 2 pad 1

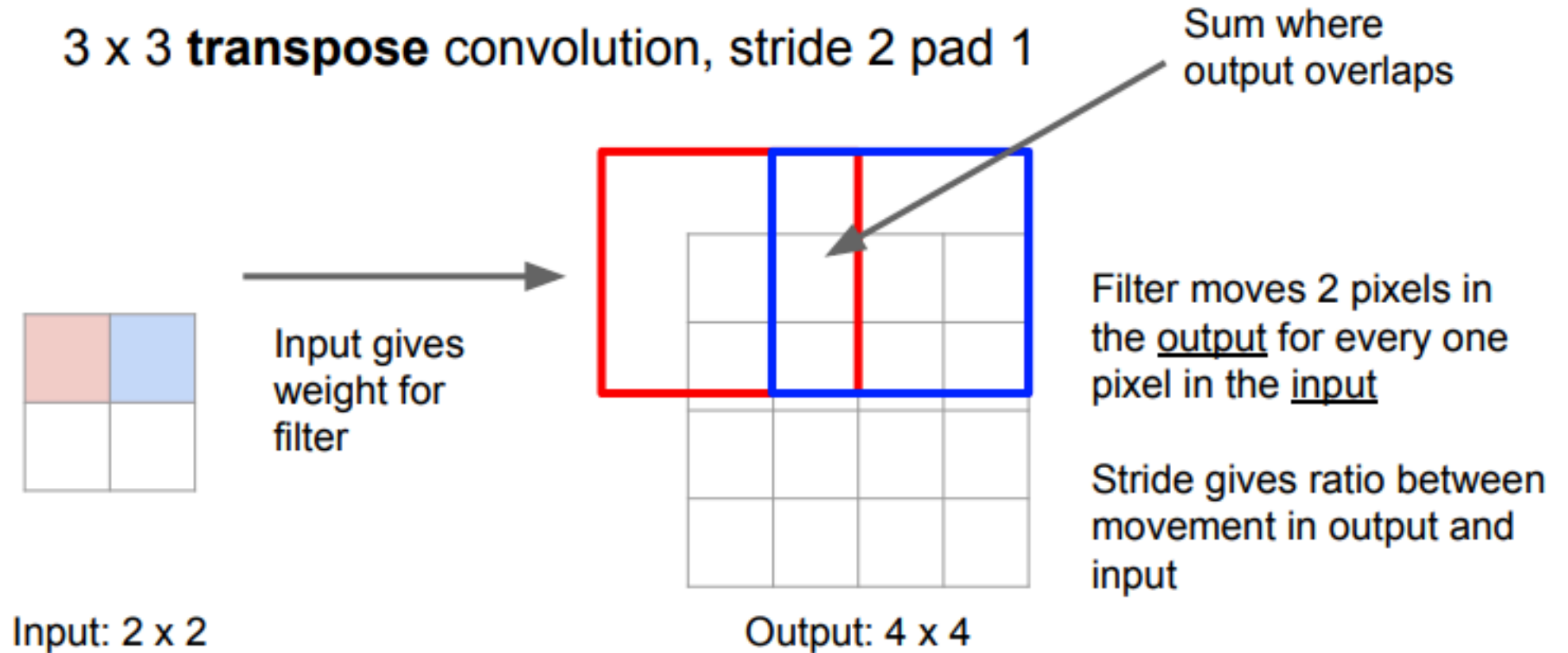


# Learnable Upsampling: Transposed Convolution

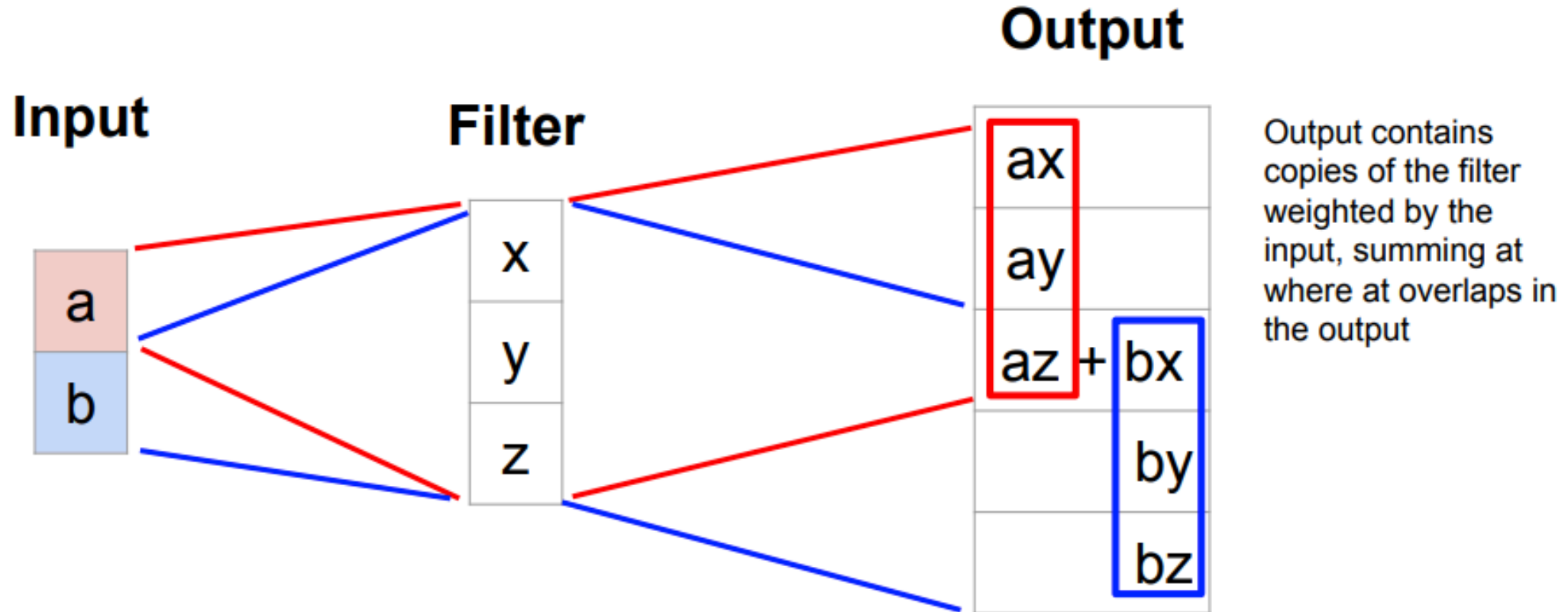


# Learnable Upsampling: Transposed Convolution

Q: Why is it called transpose convolution?



# Learnable Upsampling: Transposed Convolution



# Convolution as Matrix Multiplication

We can express convolution in terms of a matrix multiplication

$$\vec{x} * \vec{a} = X \vec{a}$$

$$\begin{bmatrix} x & y & z & 0 & 0 & 0 \\ 0 & 0 & 0 & x & y & z \end{bmatrix} \begin{bmatrix} 0 \\ a \\ b \\ c \\ d \\ 0 \end{bmatrix} = \begin{bmatrix} ay + bz \\ bx + cy + dz \end{bmatrix}$$

Example: 1D conv, kernel  
size=3, stride=2, padding=1

# Convolution as Matrix Multiplication

We can express convolution in terms of a matrix multiplication

$$\vec{x} * \vec{a} = X \vec{a}$$

$$\begin{bmatrix} x & y & z & 0 & 0 & 0 \\ 0 & 0 & 0 & x & y & z \end{bmatrix} \begin{bmatrix} 0 \\ a \\ b \\ c \\ d \\ 0 \end{bmatrix} = \begin{bmatrix} ay + bz \\ bx + cy + dz \end{bmatrix}$$

Example: 1D conv, kernel size=3, stride=2, padding=1

Convolution transpose multiplies by the transpose of the same matrix:

$$\vec{x} *^T \vec{a} = X^T \vec{a}$$

$$\begin{bmatrix} x & 0 \\ y & 0 \\ z & x \\ 0 & y \\ 0 & z \\ 0 & 0 \end{bmatrix} \begin{bmatrix} a \\ b \end{bmatrix} = \begin{bmatrix} ax \\ ay \\ az + bx \\ by \\ bz \\ 0 \end{bmatrix}$$

Example: 1D transpose conv, kernel size=3, stride=2, padding=0

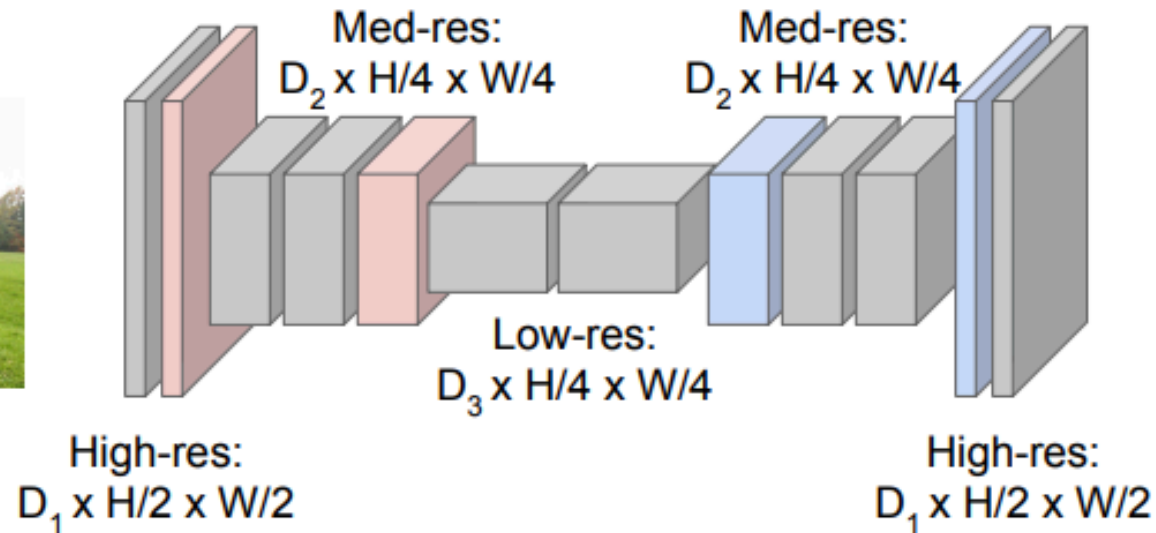
# Semantic Segmentation: Fully Convolutional

**Downsampling:**  
Pooling, strided  
convolution



Input:  
 $3 \times H \times W$

Design network as a bunch of convolutional layers, with **downsampling** and **upsampling** inside the network!



**Upsampling:**  
Unpooling or strided  
transpose convolution

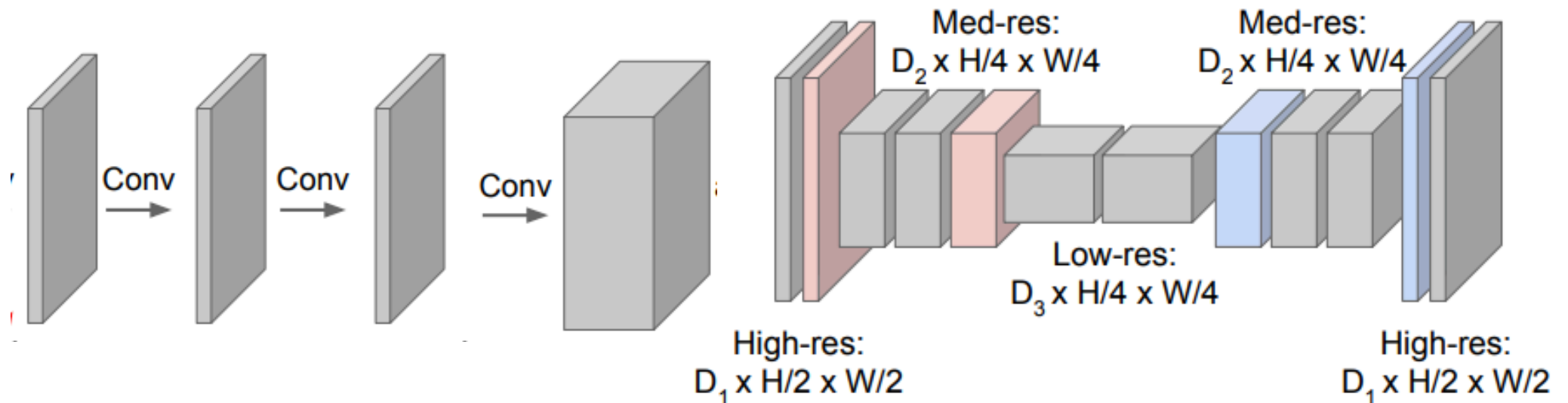


Predictions:  
 $H \times W$

Long, Shelhamer, and Darrell, "Fully Convolutional Networks for Semantic Segmentation", CVPR 2015  
Noh et al, "Learning Deconvolution Network for Semantic Segmentation", ICCV 2015

# Advantage of Bottleneck

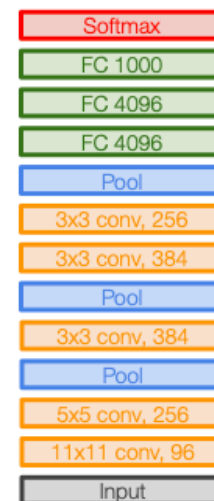
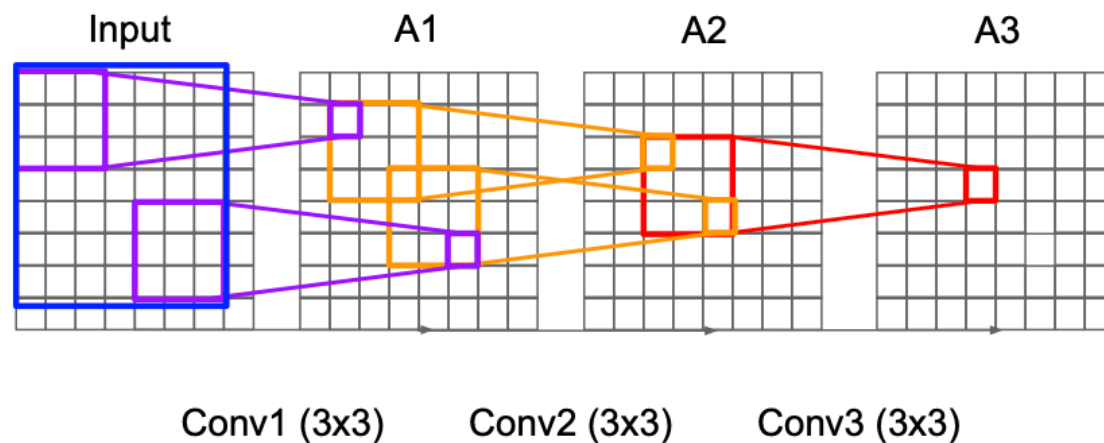
- Lower memory cost



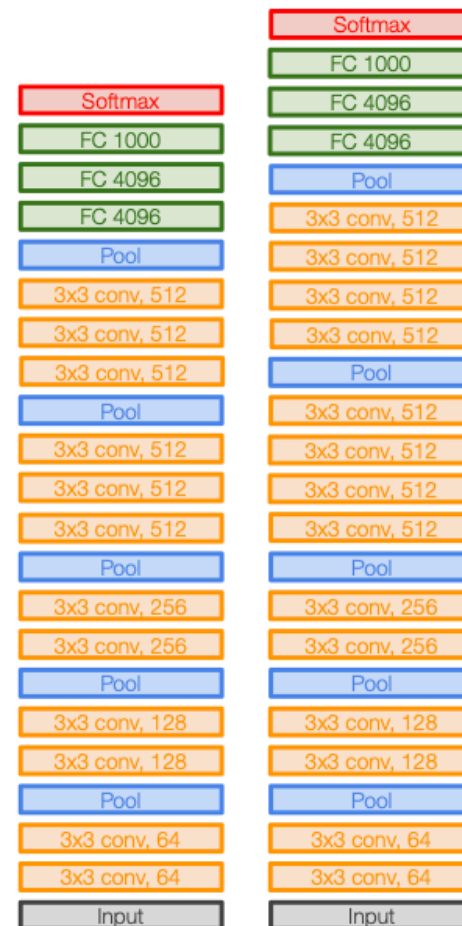


# Recap of Receptive Field

Q: What is the effective receptive field of three 3x3 conv (stride 1) layers?



AlexNet

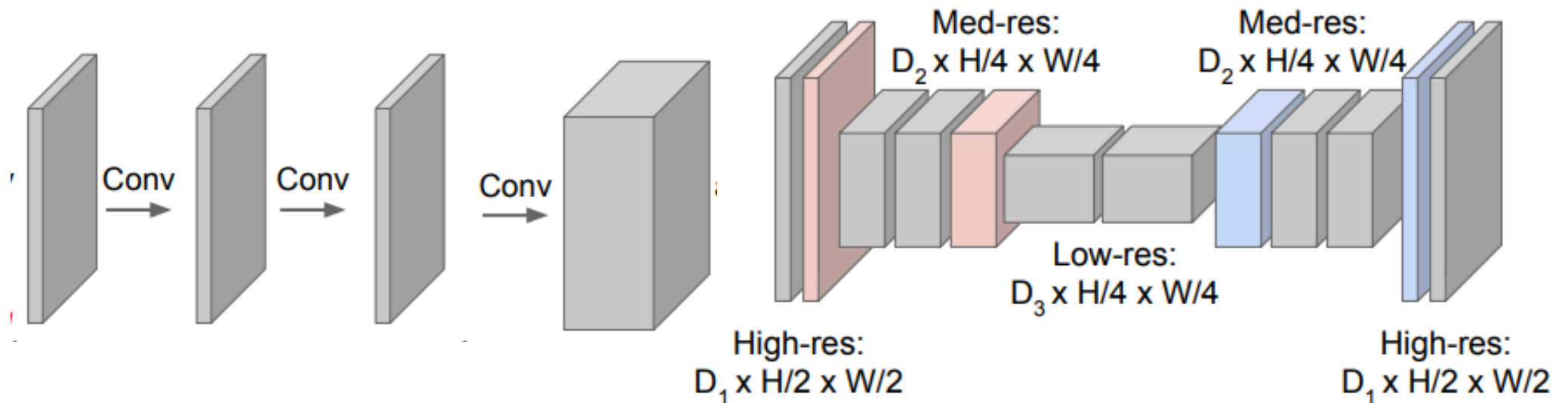


VGG16

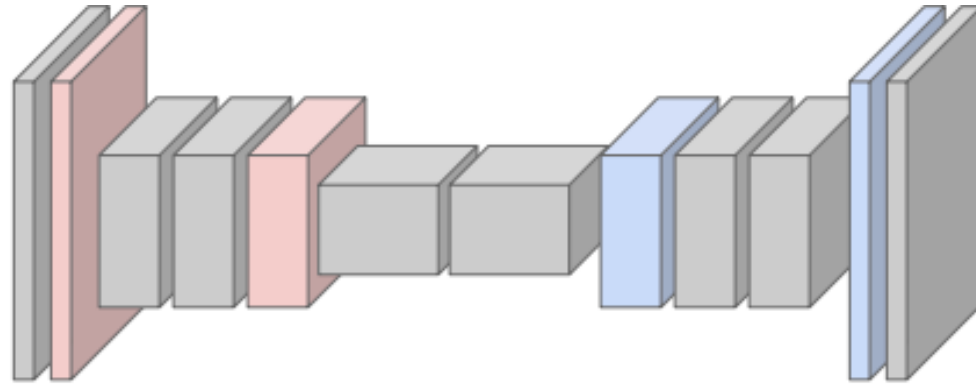
VGG19

# Advantage of Bottleneck

- Lower memory cost
- Larger receptive field and thus better global context
  - Convolution on a smaller feature map correspond to conv with a big kernel size at the original resolution

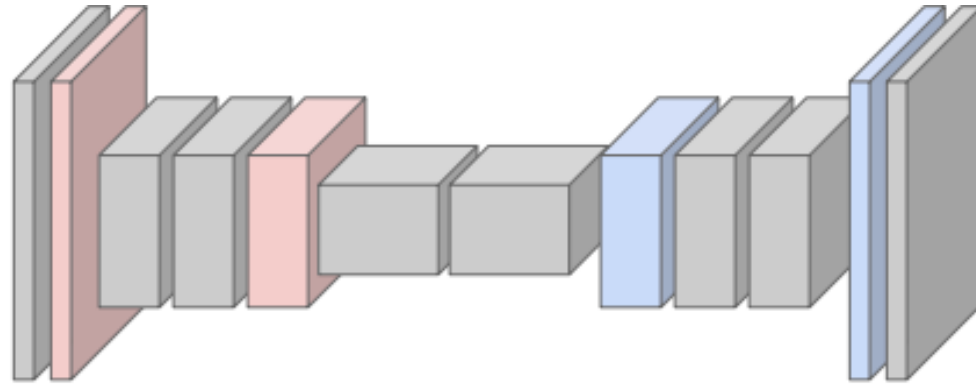


# Improving FCN



What needs to be stored in the bottleneck?

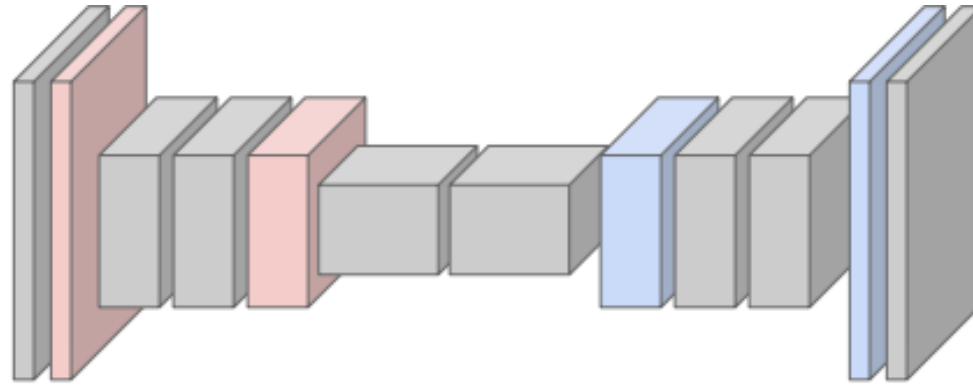
# Improving FCN



What needs to be stored in the bottleneck?

- Global context

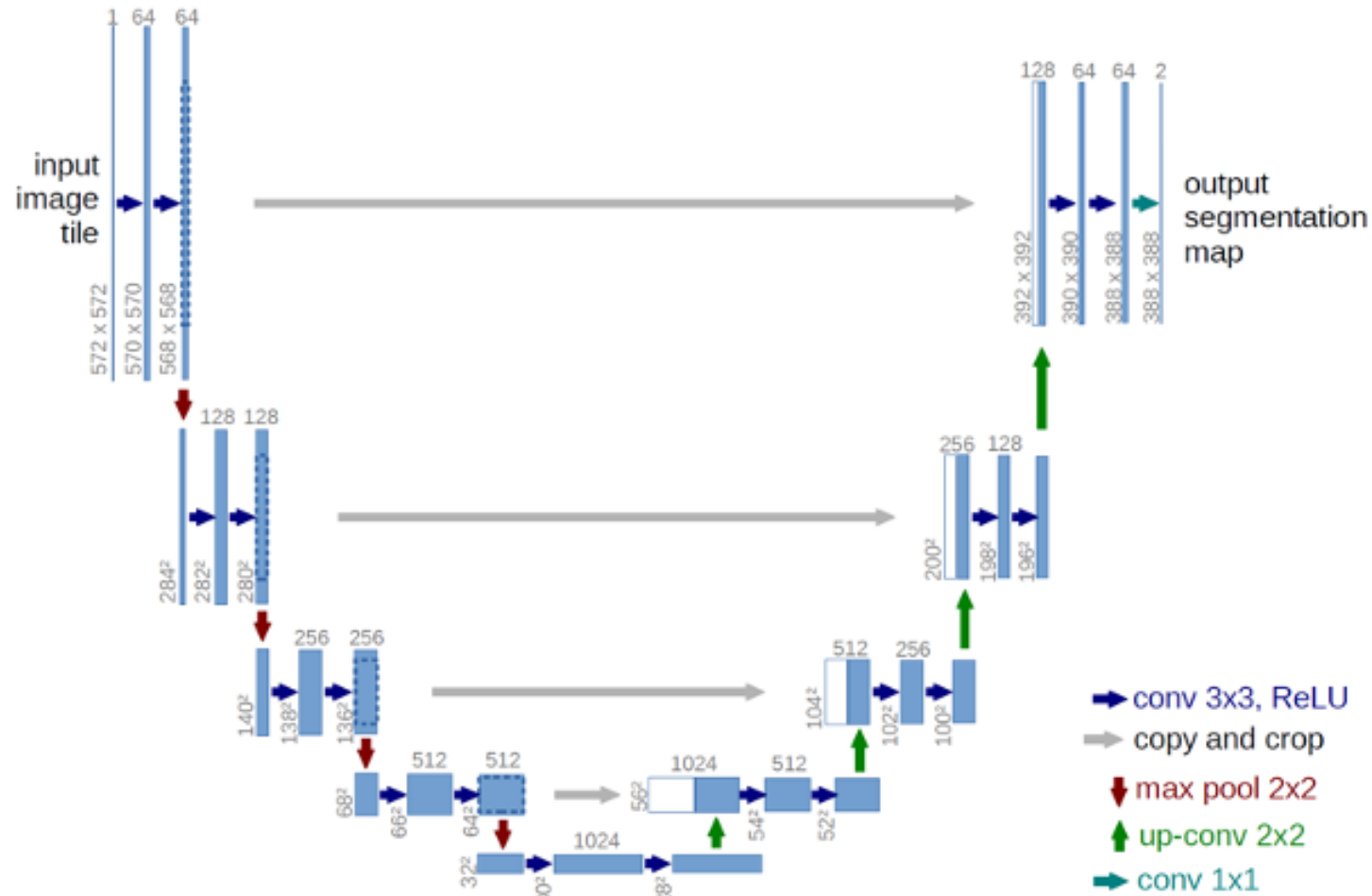
# Improving FCN



What needs to be stored in the bottleneck?

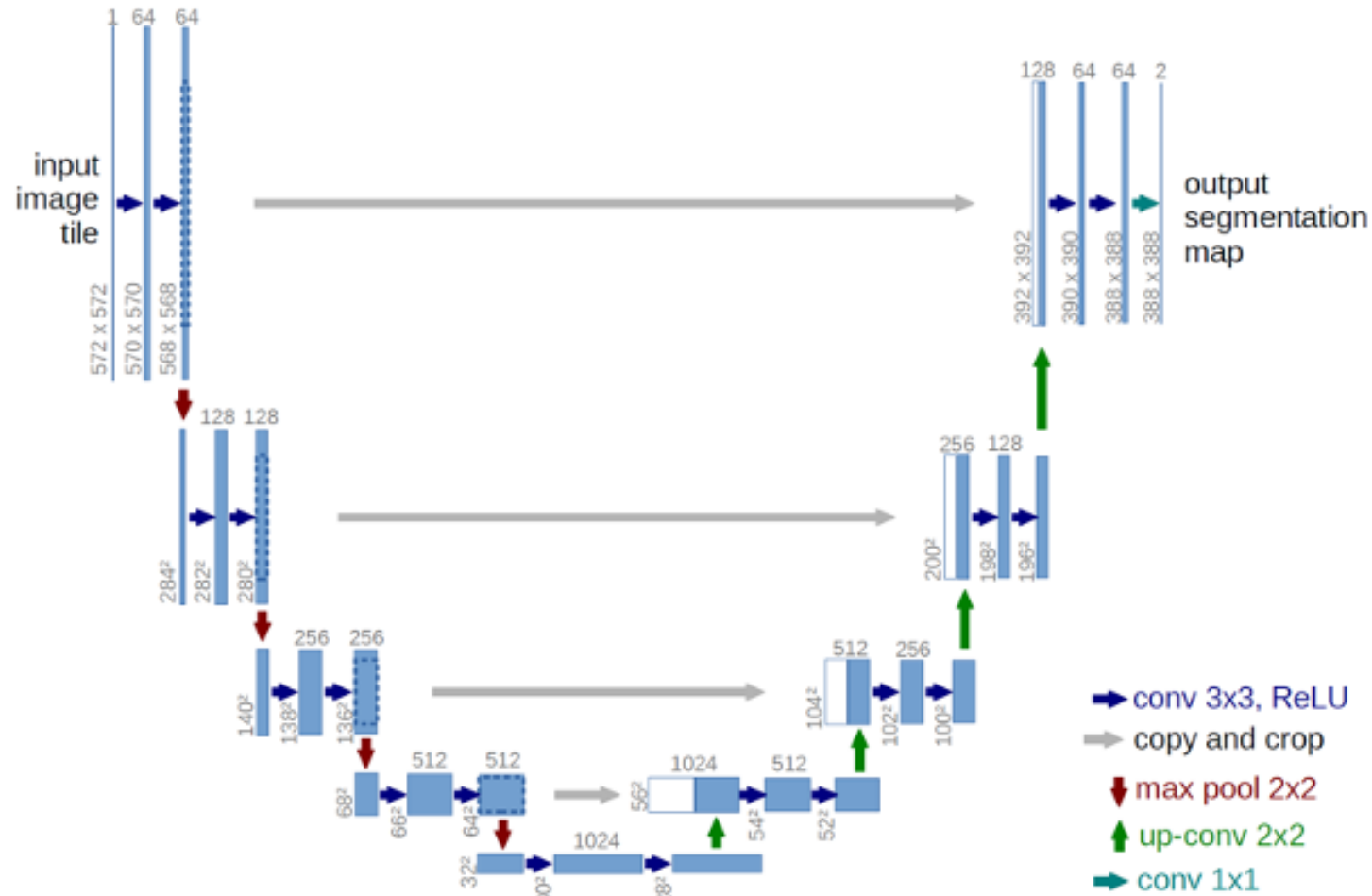
- Global context
- Per-pixel spatial information, especially around the boundary

# UNet Structure



- Skip link between the feature maps from the encoder and the decoder with the same resolution.
- Now what needs to store in the bottleneck?

# UNet Structure



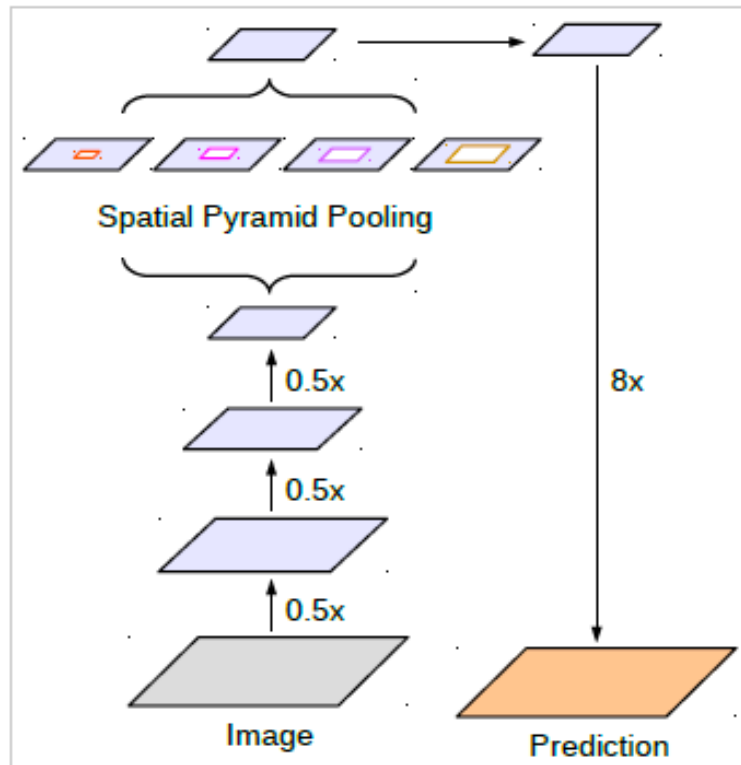
- The skip link makes shortcut from the inputs to the outputs
- Bottleneck: no need to memorize the whole image but only provides global context

# Summary of Semantic Segmentation

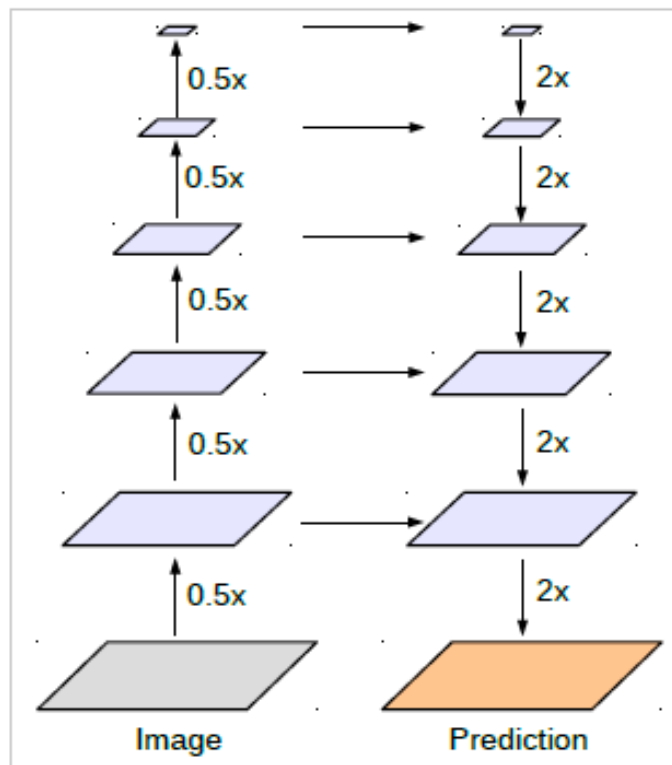
- A top-down approach
- Bottleneck structure:
  - Large receptive field and provides global context
  - Get rid of redundant information
  - Lower the computation cost
- Skip link:
  - Assist final segmentation
  - Avoid memorization



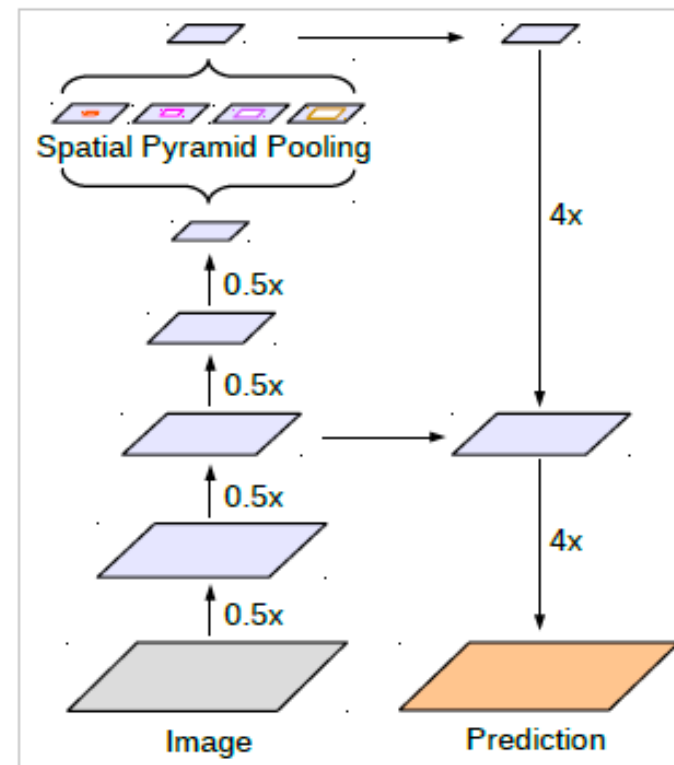
# DeepLab V3



(a) Spatial Pyramid Pooling

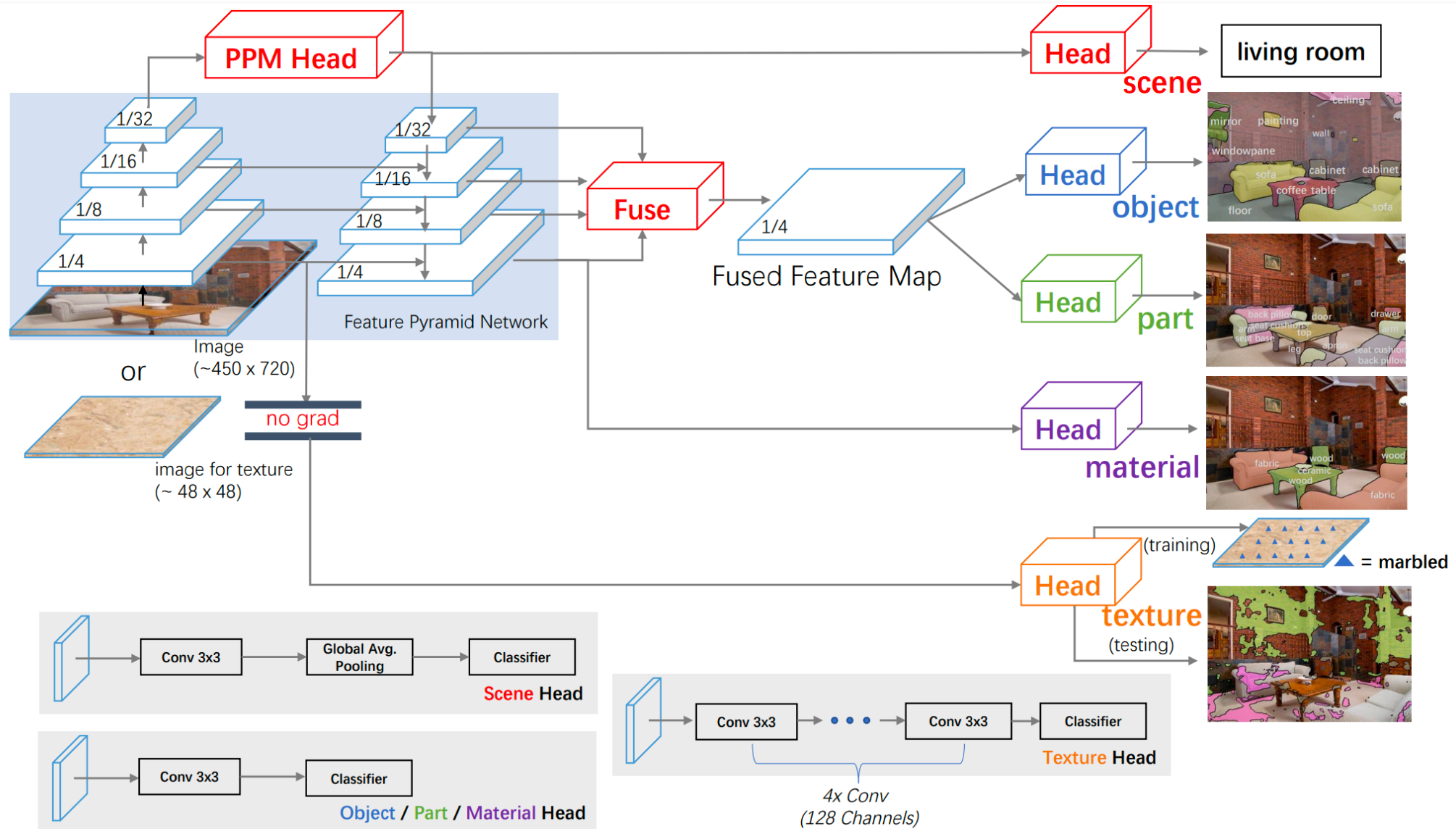


(b) Encoder-Decoder



(c) Encoder-Decoder with Atrous Conv

# General Dense Prediction: UperNet



# Evaluation Metrics: Pixel Accuracy

- Pixel accuracy: simply report the percent of pixels in the image which were correctly classified.

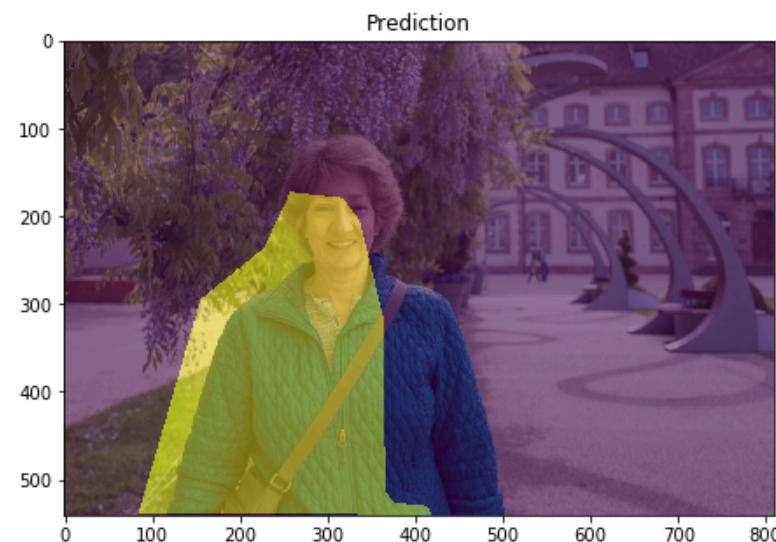
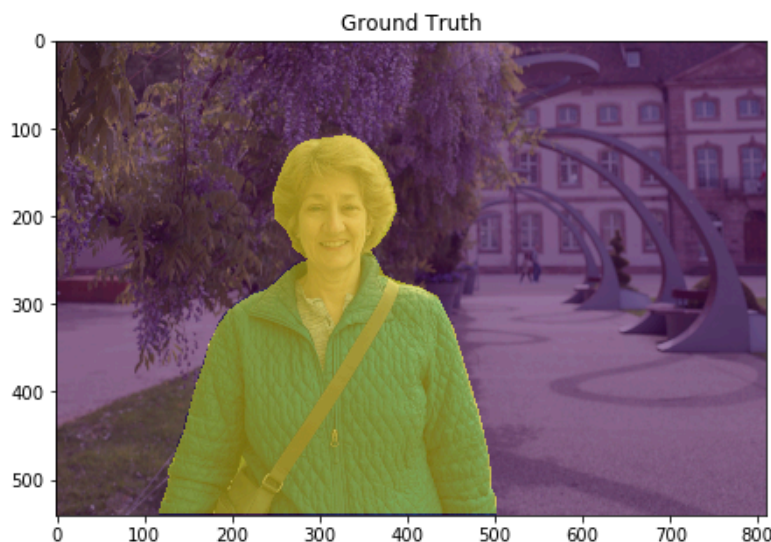
$$accuracy = \frac{TP + TN}{TP + TN + FP + FN}$$

- However, may be misleading when the class representation is small within the image, as the measure will be biased in mainly reporting how well you identify negative case (ie. where the class is not present).

# Evaluation Metrics: Intersection over Union

- Intersection over Union

$$IoU = \frac{target \cap prediction}{target \cup prediction}$$



# Alternative Loss: Soft IoU Loss

$$IoU = \frac{I(X)}{U(X)} .$$

where,  $I(X)$  and  $U(X)$  can be approximated as follows:

$$I(X) = \sum_{v \in V} X_v * Y_v .$$

$$U(X) = \sum_{v \in V} (X_v + Y_v - X_v * Y_v) .$$

Therefore, the IoU loss  $L_{IoU}$  can be defined as follows:

$$L_{IoU} = 1 - IoU = 1 - \frac{I(X)}{U(X)} .$$

# Evaluation Metrics: mIoU

- For each class, we can compute the metrics above by finding the intersection between the ground truth and predicted one-hot encoded masks for each class.
- Metrics can be examined class-by-class, or by taking the average over all the classes, to get a mean IoU.

# Introduction to Computer Vision



Next week: Lecture 9,  
3D Vision I

